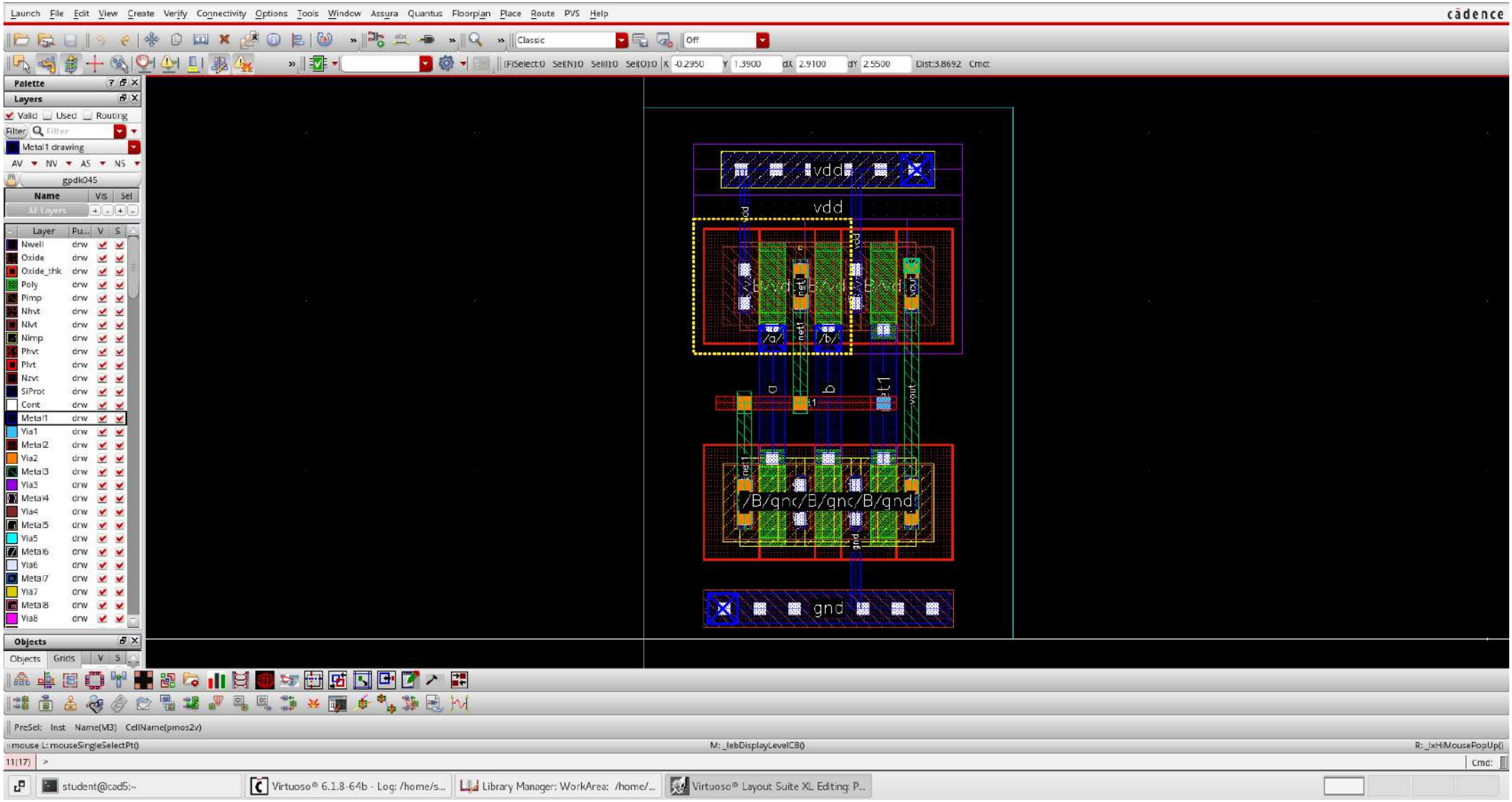
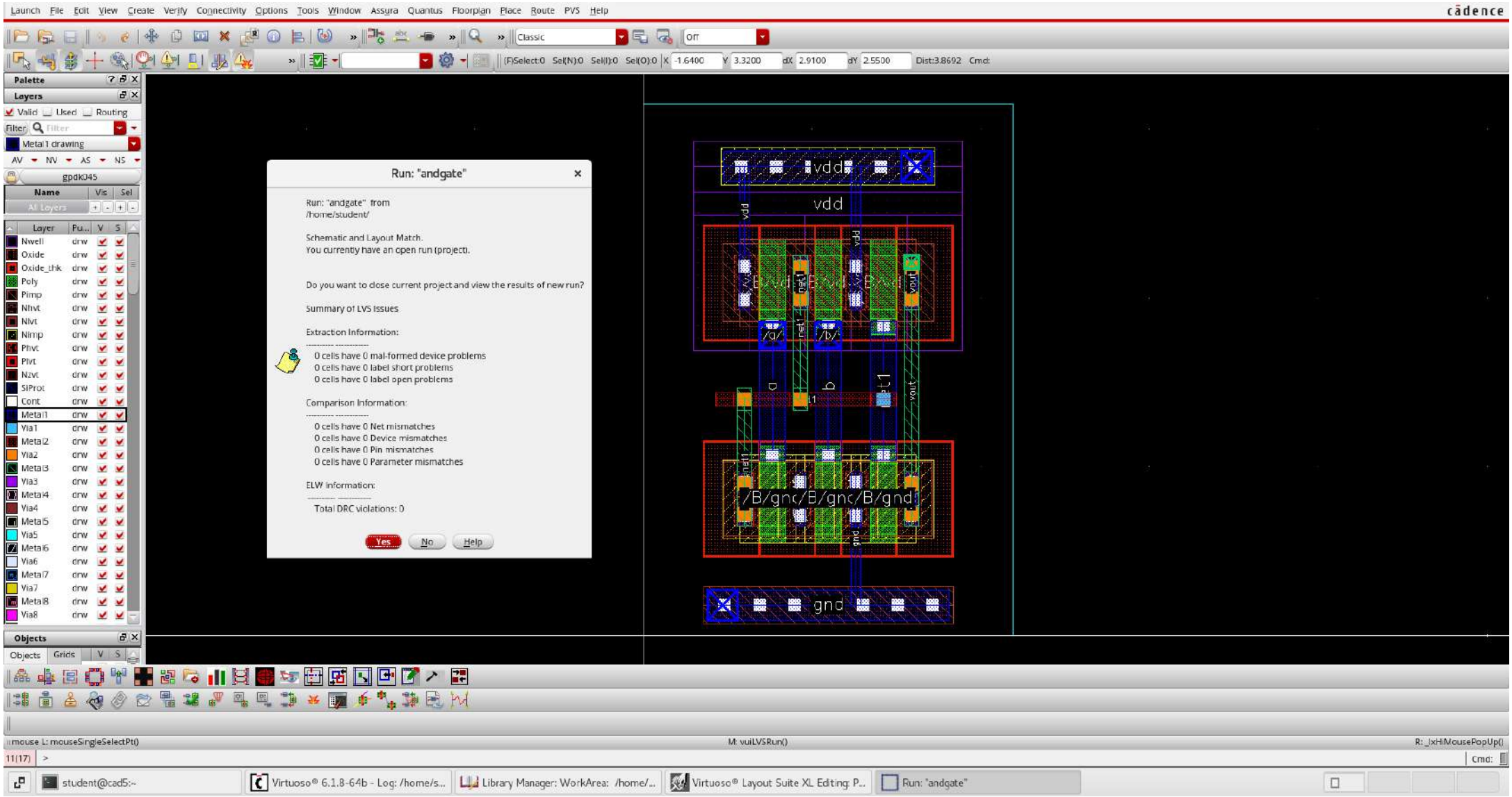
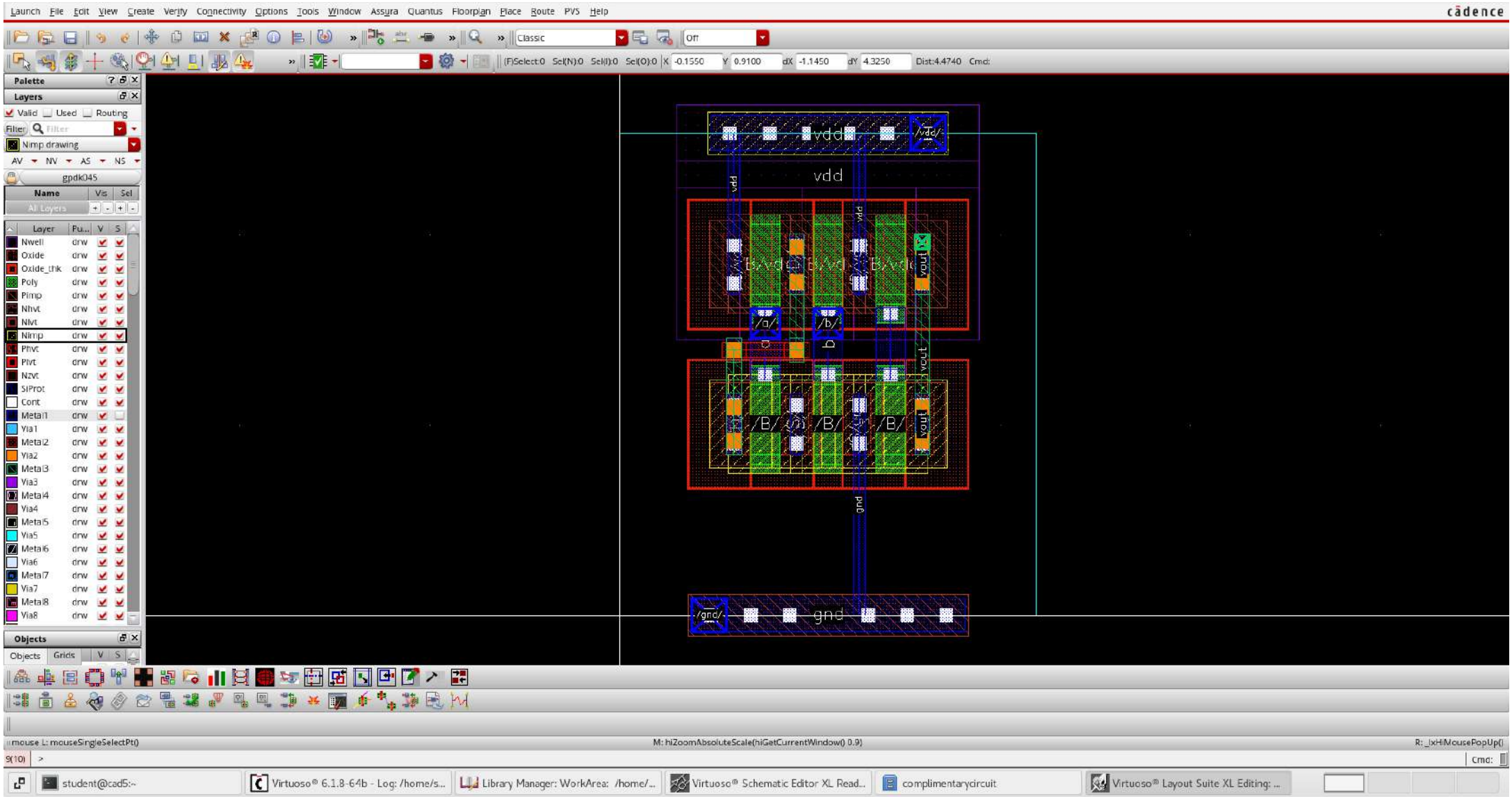
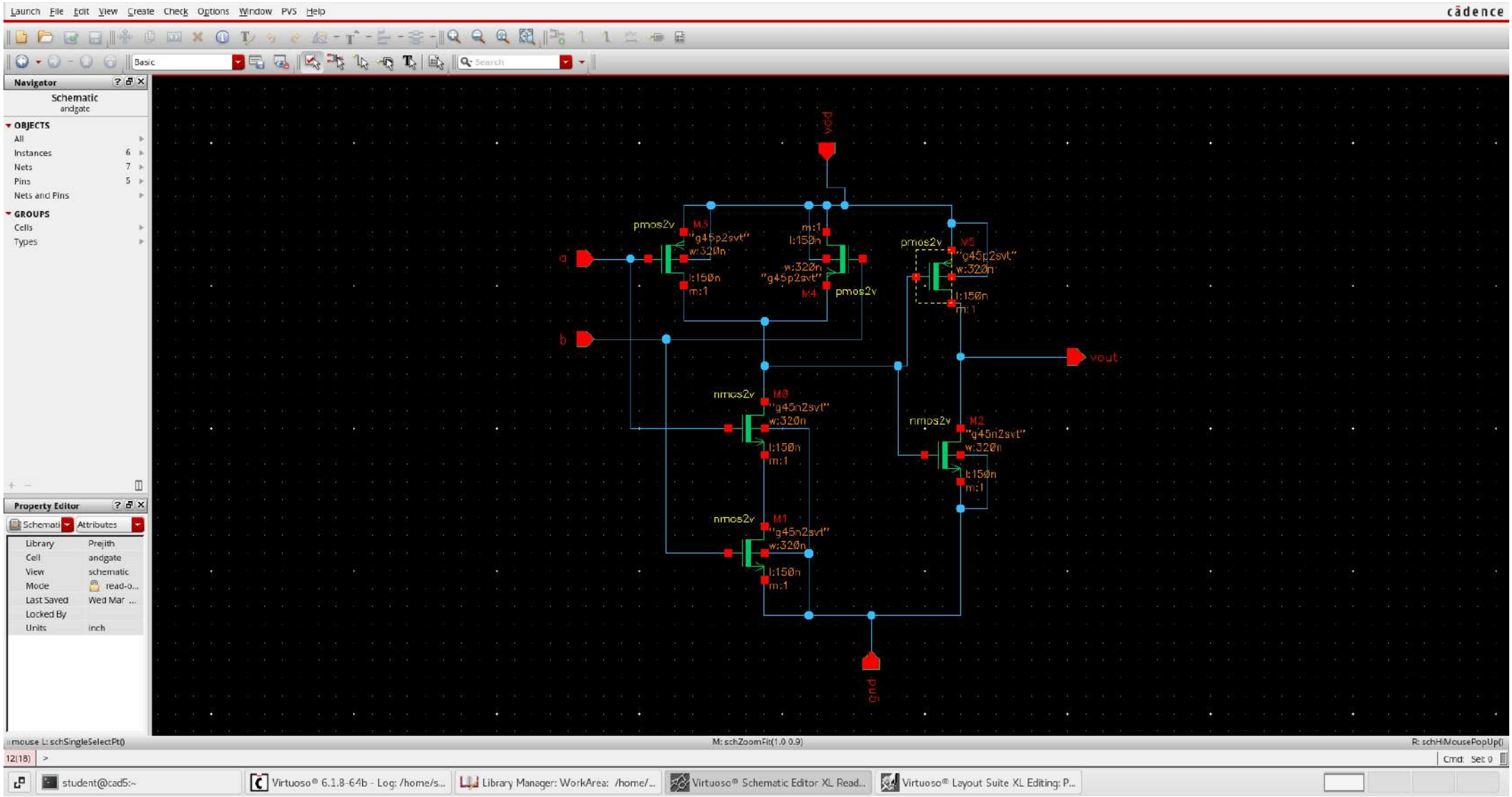
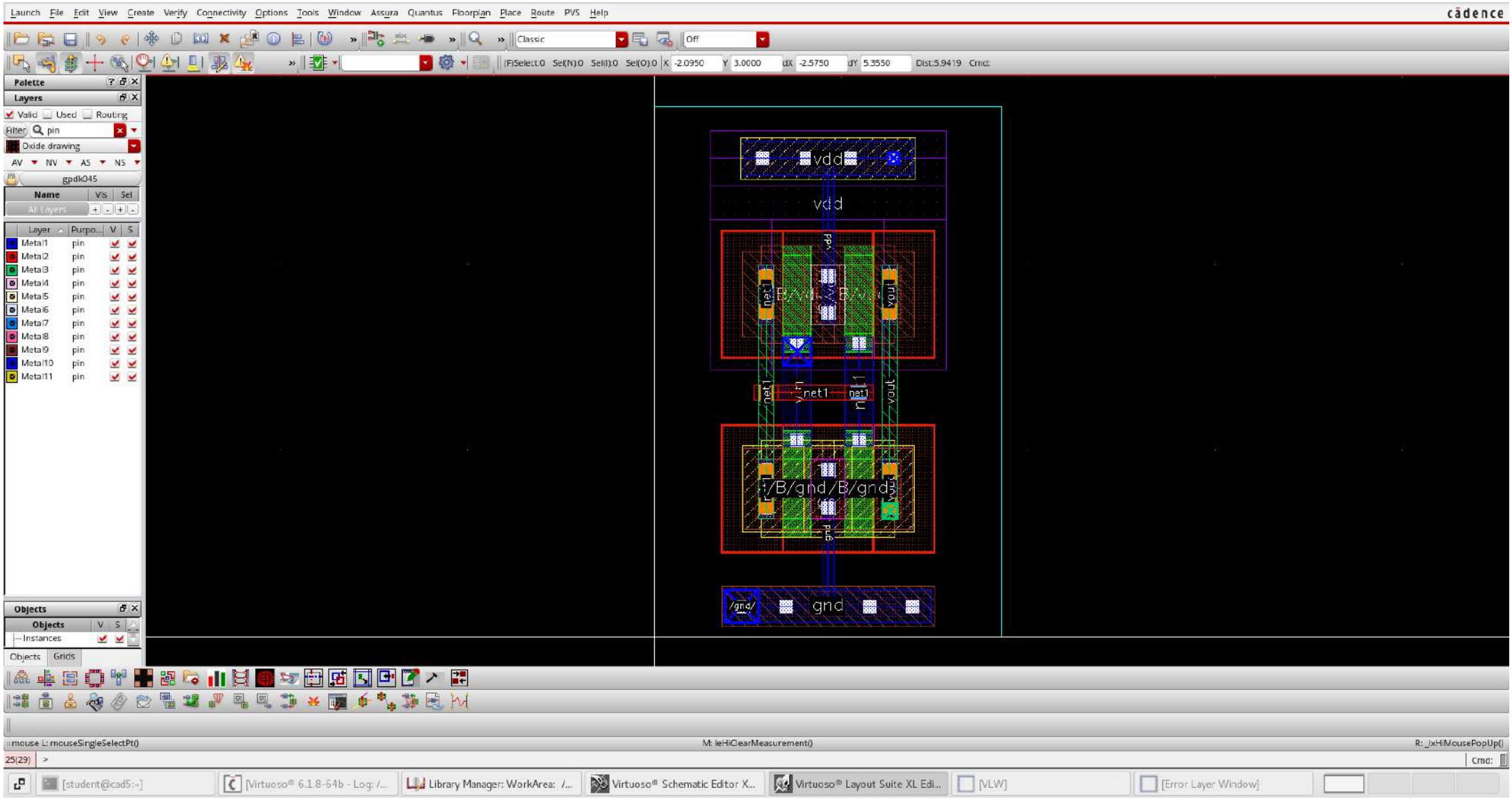
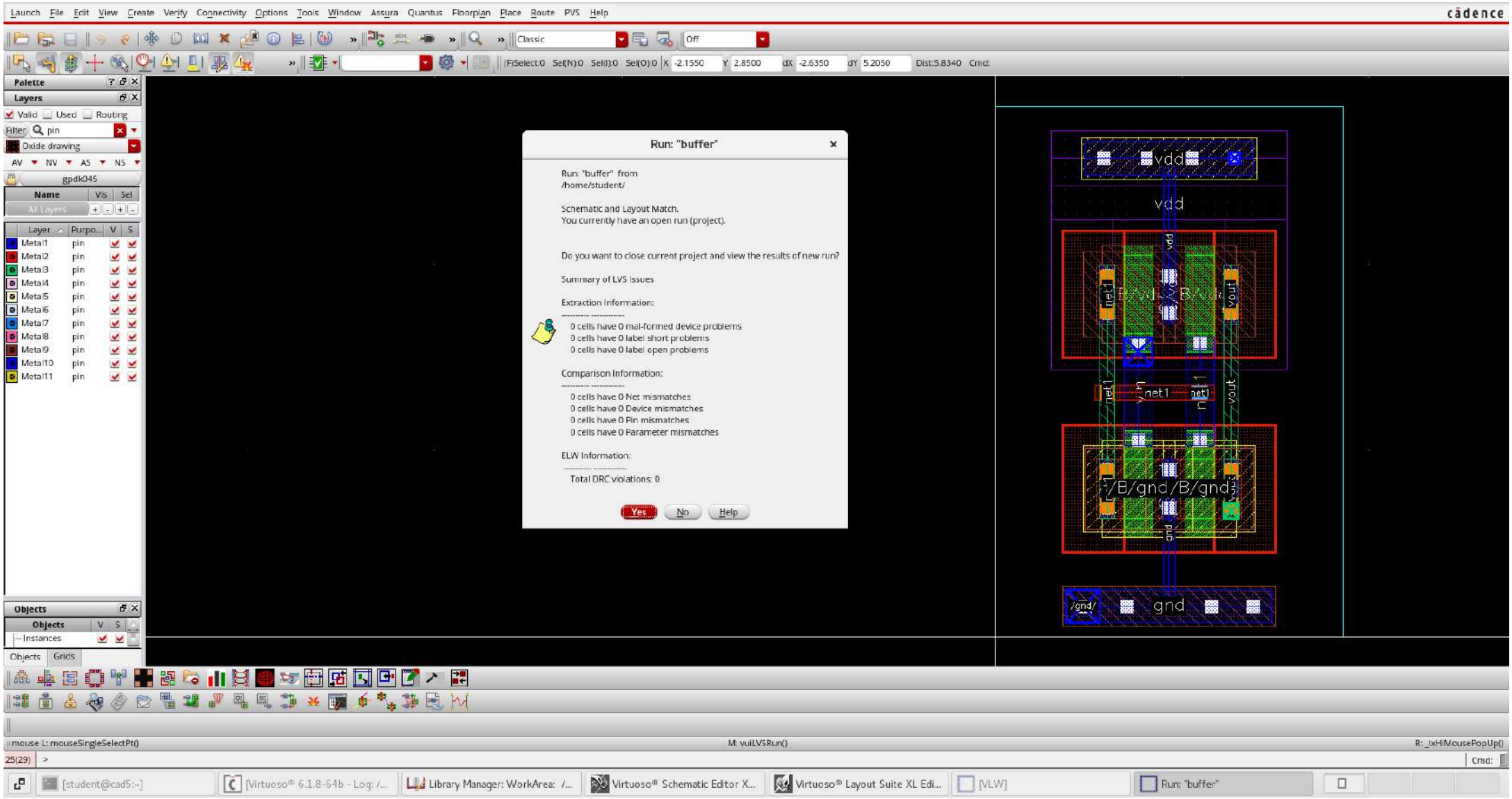

Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Valid Used Routing
Metal1 drawing
gpdk045
Name Vis Sel
Layer
Nwell
Oxide
Oxide_thk
Poly
Pimp
Nhvt
Nlvt
Nimp
Phvt
Plvt
Nzvt
SiProt
Cont
Metal1
Via1
Metal2
Via2
Metal3
Via3
Metal4
Via4
Metal5
Via5
Metal6
Via6
Metal7
Via7
Metal8
Via8
Objects
vdd
gnd
/a/
/b/
/B/gnd/B/gnd/B/gnd
PreSel: Inst Name(M3) CellName(pmos2v)
mouse L: mouseSingleSelectPt()
M: _lebDisplayLevelCB()
R: _lxHiMousePopUp()
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /home/s...
Library Manager: WorkArea: /home/...
Virtuoso® Layout Suite XL Editing: P...

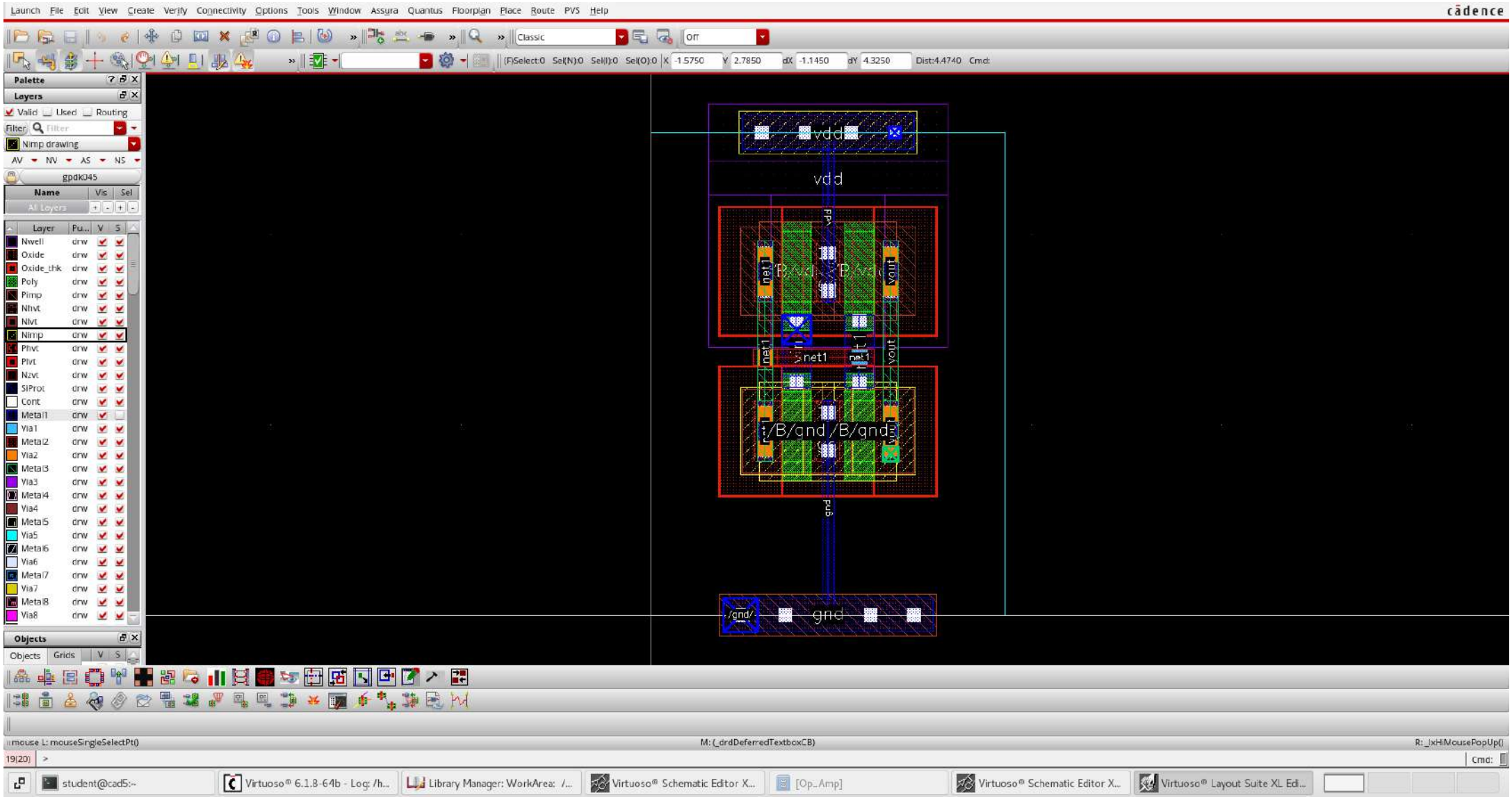

Run: "andgate"
Run: "andgate" from
/home/student/
Schematic and Layout Match.
You currently have an open run (project).
Do you want to close current project and view the results of new run?
Summary of LVS Issues
Extraction Information:
0 cells have 0 mal-formed device problems
0 cells have 0 label short problems
0 cells have 0 label open problems
Comparison Information:
0 cells have 0 Net mismatches
0 cells have 0 Device mismatches
0 cells have 0 Pin mismatches
0 cells have 0 Parameter mismatches
ELW Information:
Total DRC violations: 0
Yes
No
Help
vdd
gnd
/B/gnd/B/gnd/B/gnd

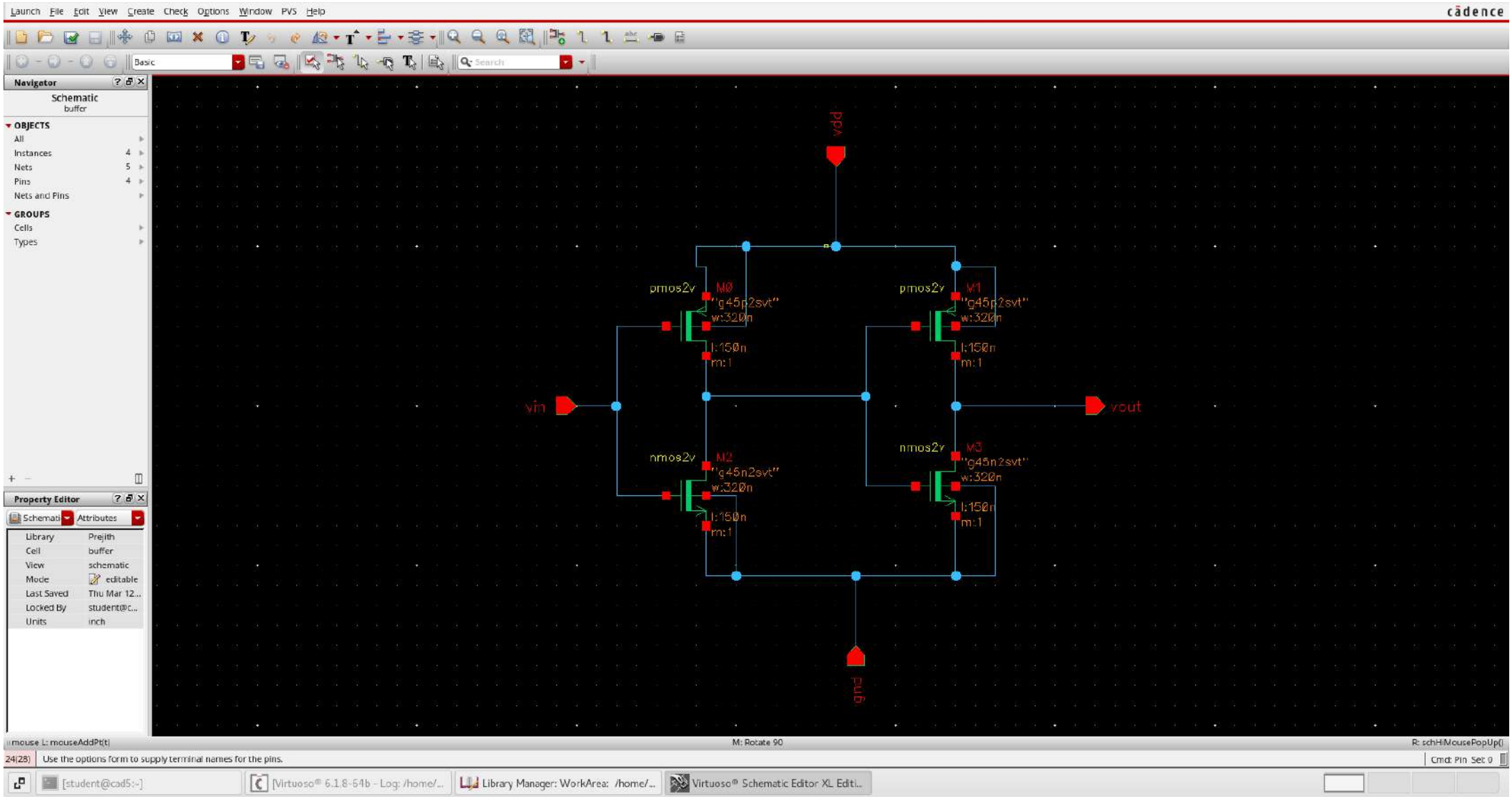

Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Valid Used Routing
Nimp drawing
gpdk045
Name Vis Sel
All Layers
Layer Pu... V S
Nwell drw
Oxide drw
Oxide_thk drw
Poly drw
Pimp drw
Nhvt drw
Nlvt drw
Nimp drw
Phvt drw
Plvt drw
Nzvt drw
SIProt drw
Cont drw
Metal1 drw
Via1 drw
Metal2 drw
Via2 drw
Metal3 drw
Via3 drw
Metal4 drw
Via4 drw
Metal5 drw
Via5 drw
Metal6 drw
Via6 drw
Metal7 drw
Via7 drw
Metal8 drw
Via8 drw
Objects
Objects Grids V S
vdd
/vdd/
vdd
vout
/a/
/b/
a
b
vout
gnd
/gnd/
gnd
mouse L: mouseSingleSelectPt()
M: hiZoomAbsoluteScale(hiGetCurrentWindow() 0.9)
R: _lxHiMousePopUp()
Cmd:
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /home/s...
Library Manager: WorkArea: /home/...
Virtuoso® Schematic Editor XL Read...
complimentarycircuit
Virtuoso® Layout Suite XL Editing: ...

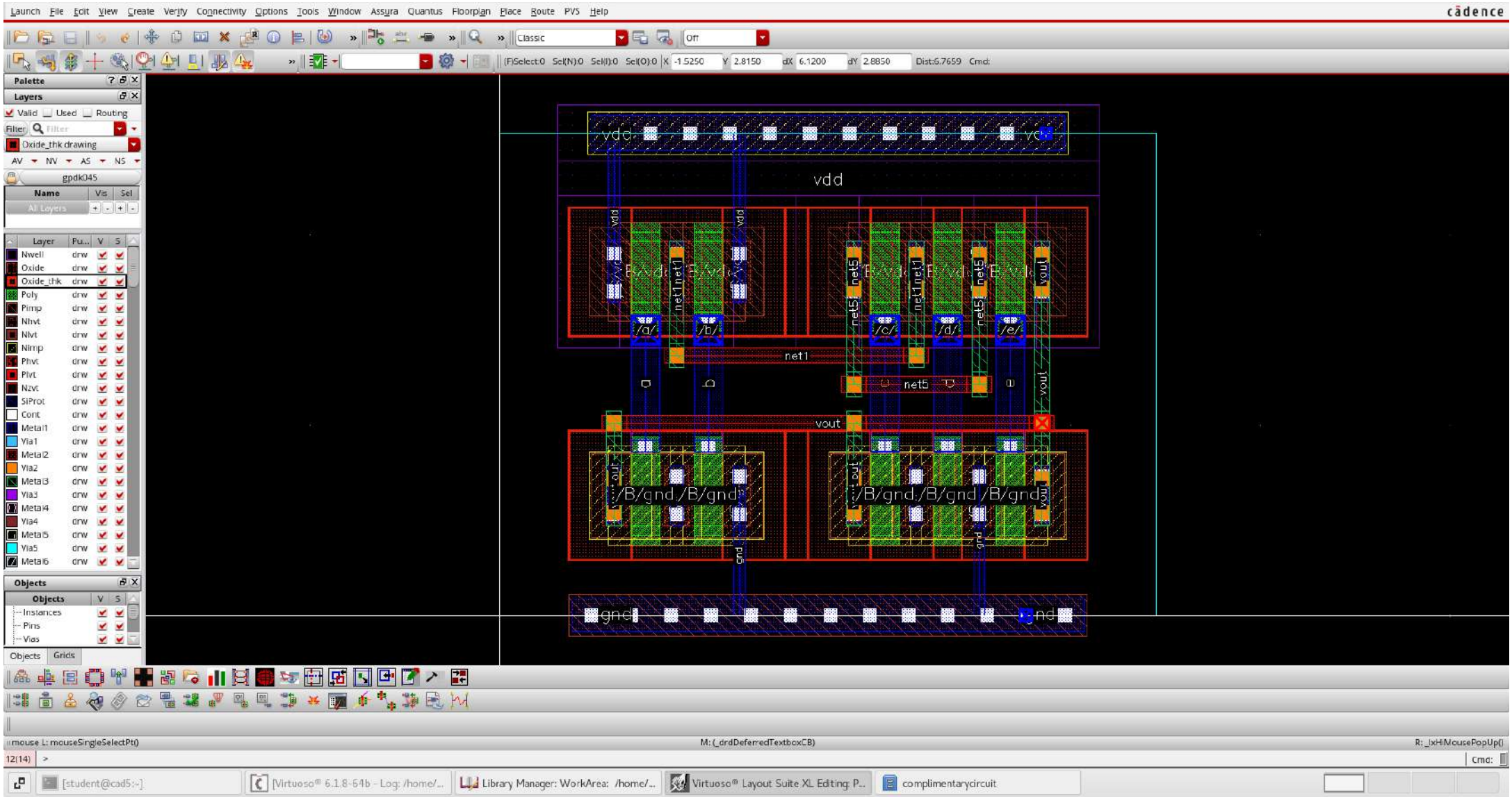

Launch File Edit View Create Check Options Window PVS Help
cadence
Navigator
Schematic
andgate
OBJECTS
All
Instances 6
Nets 7
Pins 5
Nets and Pins
GROUPS
Cells
Types
Property Editor
Schemati
Attributes
Library Prejith
Cell andgate
View schematic
Mode read-o...
Last Saved Wed Mar ...
Locked By
Units inch
vdd
gnd
a
b
vout
pmos2v
M3
"g45p2svt"
w:320n
l:150n
m:1
m:1
l:150n
w:320n
"g45p2svt"
M4
pmos2v
pmos2v
M5
"g45p2svt"
w:320n
l:150n
m:1
nmos2v
M0
"g45n2svt"
w:320n
l:150n
m:1
nmos2v
M2
"g45n2svt"
w:320n
l:150n
m:1
nmos2v
M1
"g45n2svt"
w:320n
l:150n
m:1
mouse L: schSingleSelectPt()
M: schZoomFit(1.0 0.9)
R: schHiMousePopUp()
12(18)
Cmd: Set: 0
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /home/s...
Library Manager: WorkArea: /home/...
Virtuoso® Schematic Editor XL Read...
Virtuoso® Layout Suite XL Editing: P...

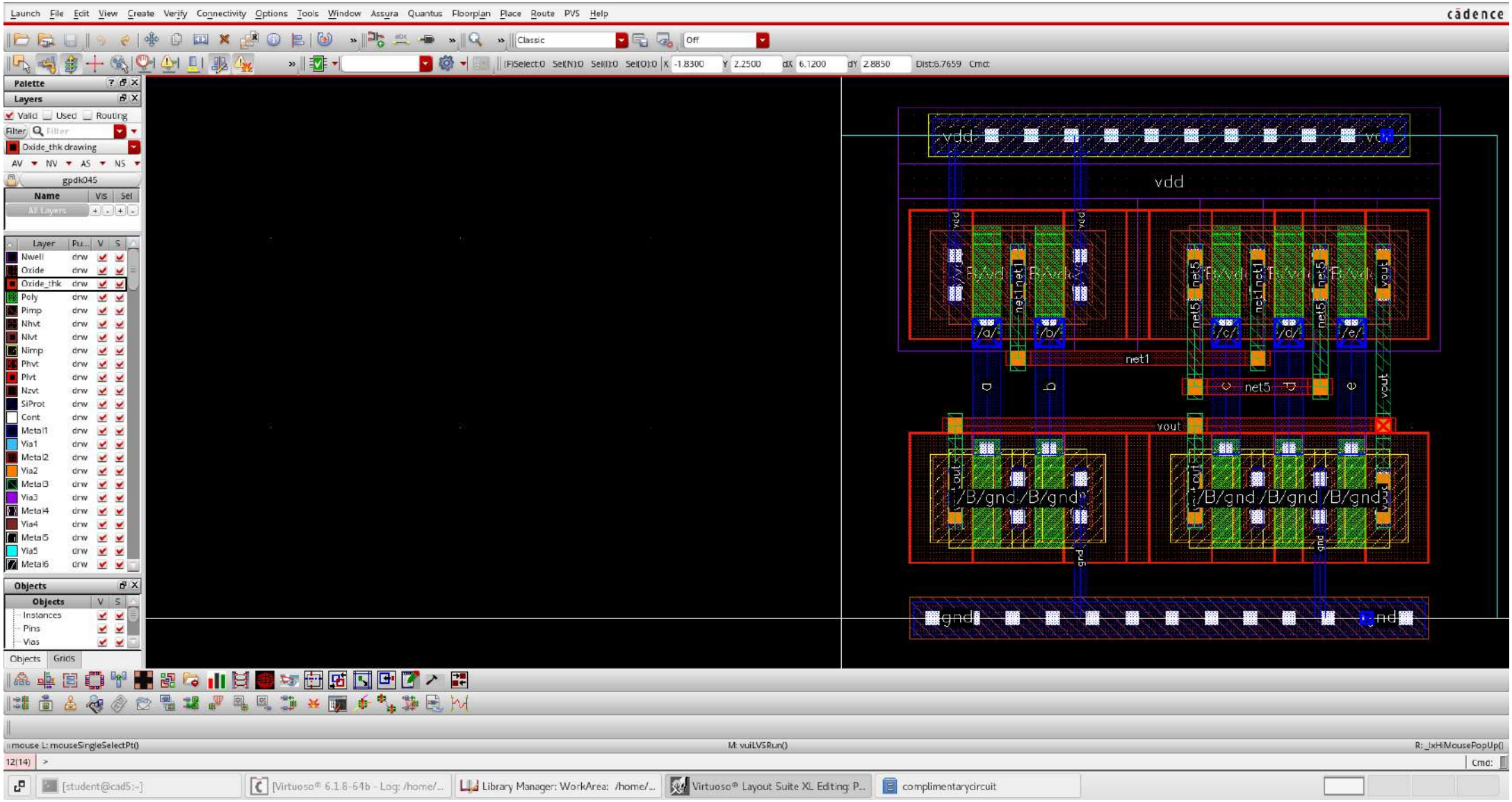

Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Valid Used Routing
Filter pin
Oxide drawing
gpdk045
Name Vis Sel
All Layers
Layer Purpo... V S
Metal1 pin
Metal2 pin
Metal3 pin
Metal4 pin
Metal5 pin
Metal6 pin
Metal7 pin
Metal8 pin
Metal9 pin
Metal10 pin
Metal11 pin
Objects
Instances
Objects Grids
vdd
vdd
net1
net1
net1
net1
/B/gnd/B/gnd
/gnd/
gnd
mouse L: mouseSingleSelectPt()
M: leHiClearMeasurement()
R: _lxHiMousePopUp()
Cmd:
[student@cad5:~]
[Virtuoso® 6.1.8-64b - Log: /...
Library Manager: WorkArea: /...
Virtuoso® Schematic Editor X...
Virtuoso® Layout Suite XL Edi...
[VLW]
[Error Layer Window]

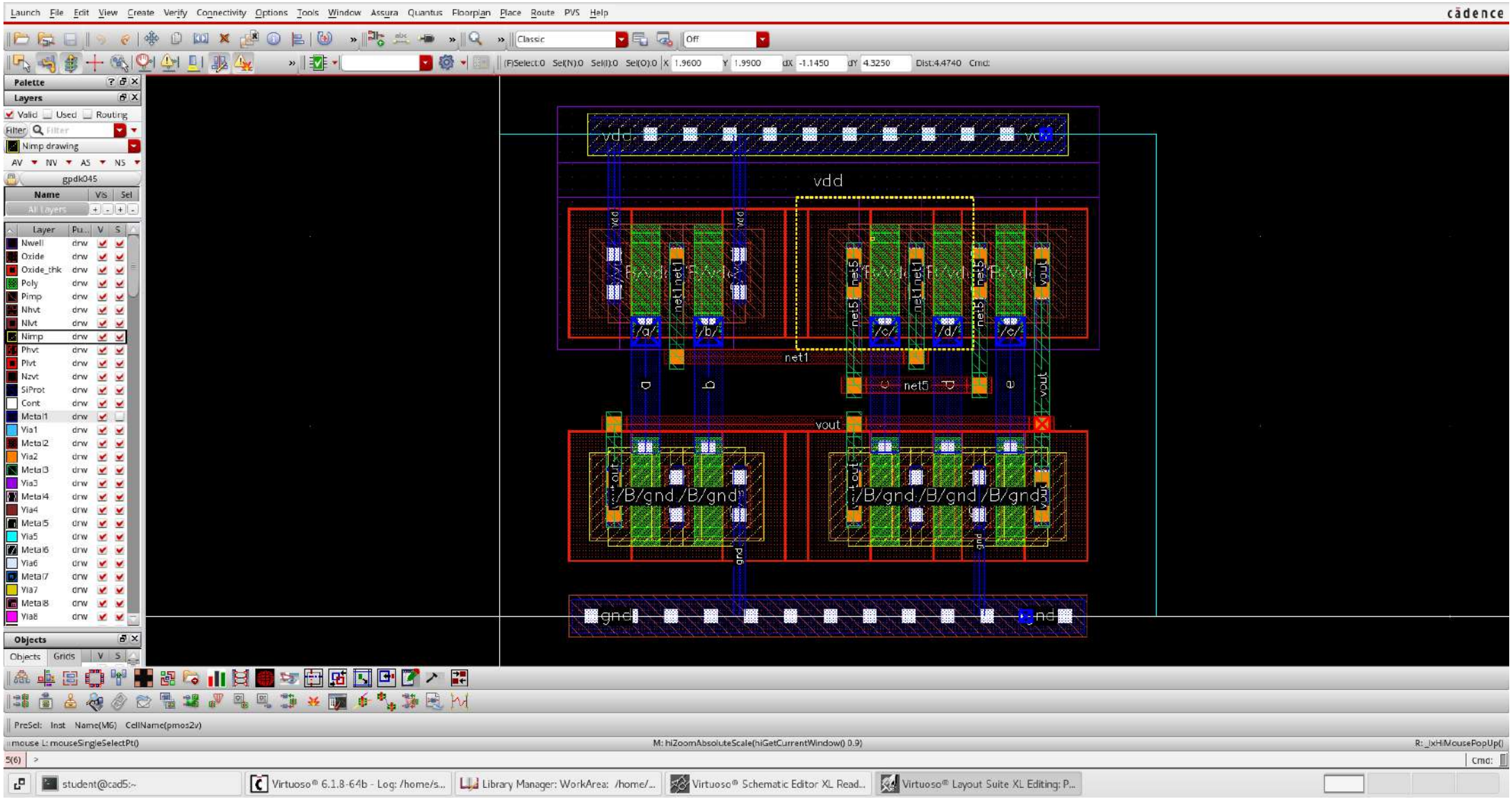

Run: "buffer"
Run: "buffer" from
/home/student/
Schematic and Layout Match.
You currently have an open run (project).
Do you want to close current project and view the results of new run?
Summary of LVS Issues
Extraction Information:
0 cells have 0 mal-formed device problems
0 cells have 0 label short problems
0 cells have 0 label open problems
Comparison Information:
0 cells have 0 Net mismatches
0 cells have 0 Device mismatches
0 cells have 0 Pin mismatches
0 cells have 0 Parameter mismatches
ELW Information:
Total DRC violations: 0
Yes
No
Help
vdd
gnd
net1
vin
vout

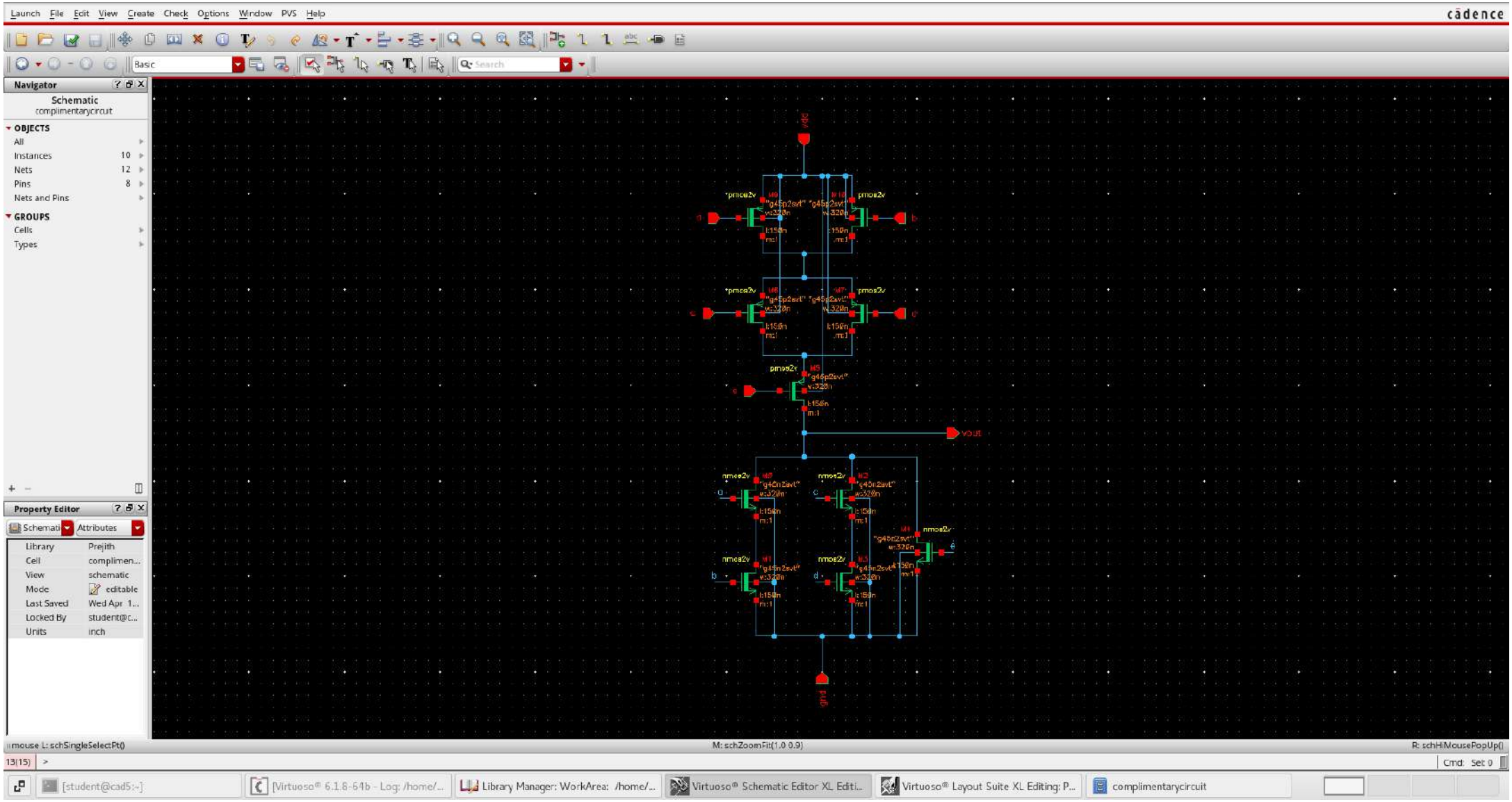

Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Valid Used Routing
Nimp drawing
gpdk045
Name Vis Sel
All Layers
Layer Pu... V S
Nwell drw
Oxide drw
Oxide_thk drw
Poly drw
Pimp drw
Nhvt drw
Nlvt drw
Nimp drw
Phvt drw
Plvt drw
Nzvt drw
SiProt drw
Cont drw
Metal1 drw
Via1 drw
Metal2 drw
Via2 drw
Metal3 drw
Via3 drw
Metal4 drw
Via4 drw
Metal5 drw
Via5 drw
Metal6 drw
Via6 drw
Metal7 drw
Via7 drw
Metal8 drw
Via8 drw
Objects
Objects Grids V S
vdd
vdd
net1
net1
net1
net1
vout
vout
/B/gnd /B/gnd
/gnd/
gnd
mouse L: mouseSingleSelectPt()
M: (_drdDeferredTextboxCB)
R: _lxHiMousePopUp()
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /h...
Library Manager: WorkArea: /...
Virtuoso® Schematic Editor X...
[Op_Amp]
Virtuoso® Schematic Editor X...
Virtuoso® Layout Suite XL Edi...

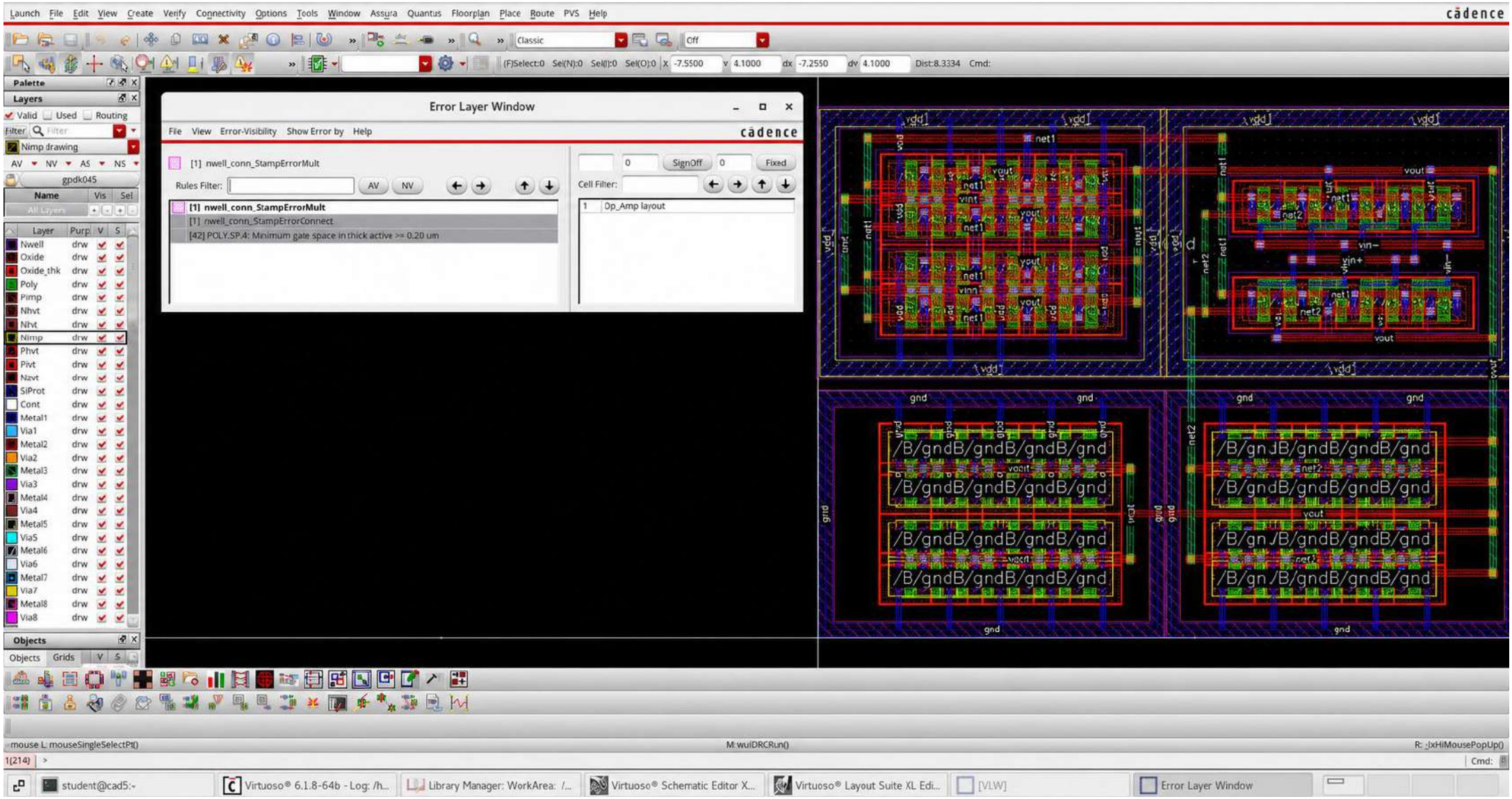

Launch File Edit View Create Check Options Window PVS Help
cadence
Navigator
Schematic
buffer
OBJECTS
All
Instances 4
Nets 5
Pins 4
Nets and Pins
GROUPS
Cells
Types
Property Editor
Schemati
Attributes
Library Prejith
Cell buffer
View schematic
Mode editable
Last Saved Thu Mar 12...
Locked By student@c...
Units inch
vdd
vin
vout
gnd
pmos2v
M0
"g45p2svt"
w:320n
l:150n
m:1
pmos2v
M1
"g45p2svt"
w:320n
l:150n
m:1
nmos2v
M2
"g45n2svt"
w:320n
l:150n
m:1
nmos2v
M3
"g45n2svt"
w:320n
l:150n
m:1
mouse L: mouseAddPt(t)
M: Rotate 90
R: schHiMousePopUp()
24(28) Use the options form to supply terminal names for the pins.
Cmd: Pin Set: 0
[student@cad5:~]
[Virtuoso® 6.1.8-64b - Log: /home/...
Library Manager: WorkArea: /home/...
Virtuoso® Schematic Editor XL Editi...

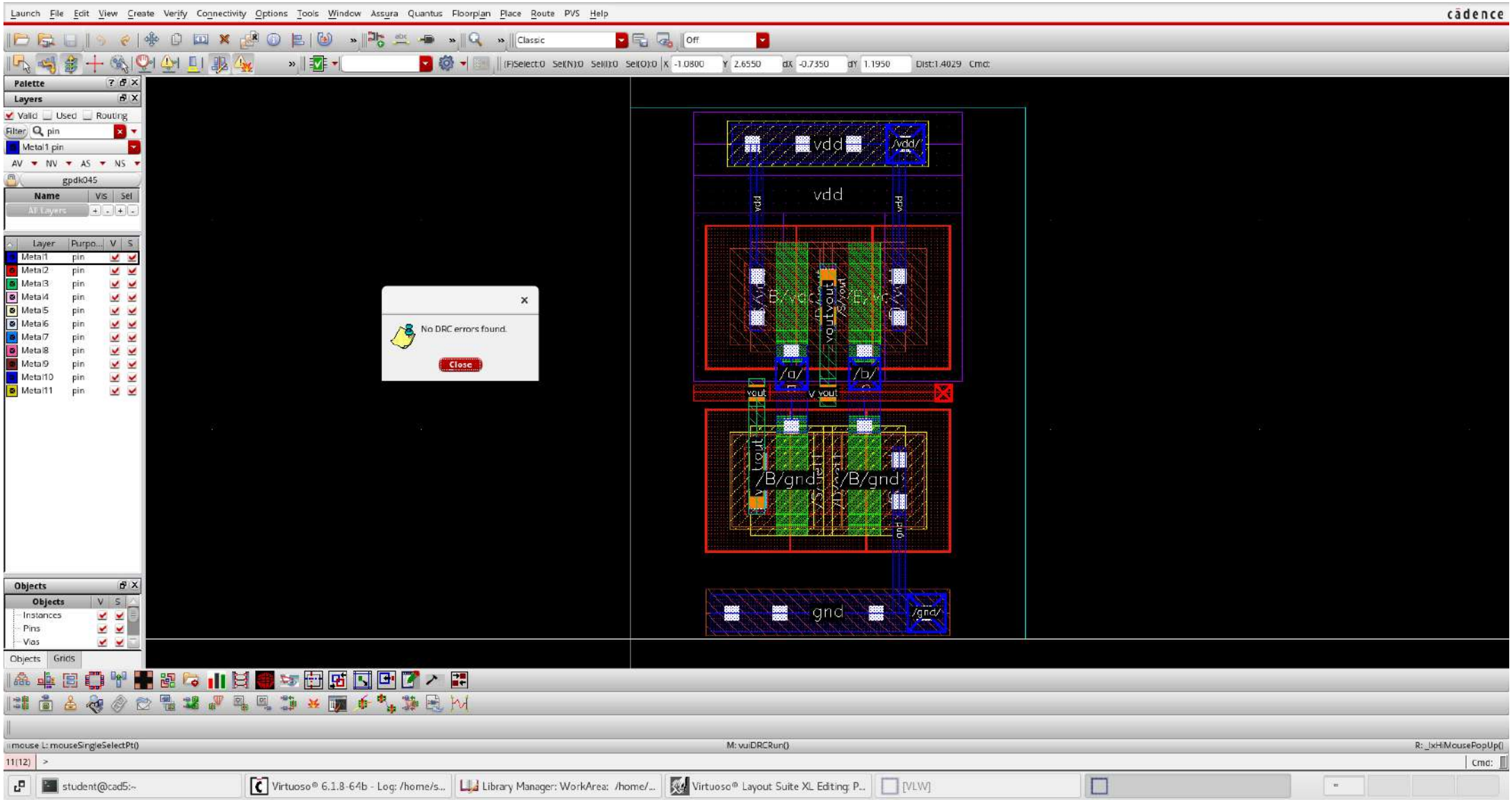

Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Valid Used Routing
Oxide_thk drawing
gpdk045
Name Vis Sel
All Layers
Layer
Nwell
Oxide
Oxide_thk
Poly
Pimp
Nhvt
Nlvt
Nimp
Phvt
Plvt
Nzvt
SiProt
Cont
Metal1
Via1
Metal2
Via2
Metal3
Via3
Metal4
Via4
Metal5
Via5
Metal6
Objects
Instances
Pins
Vias
vdd
net1
net5
vout
gnd
/B/gnd /B/gnd
Objects Grids
mouse L: mouseSingleSelectPt()
M: (_drdDeferredTextboxCB)
R: _lxHiMousePopUp()
Cmd:
[student@cad5:~]
[Virtuoso® 6.1.8-64b - Log: /home/...
Library Manager: WorkArea: /home/...
Virtuoso® Layout Suite XL Editing: P...
complimentarycircuit

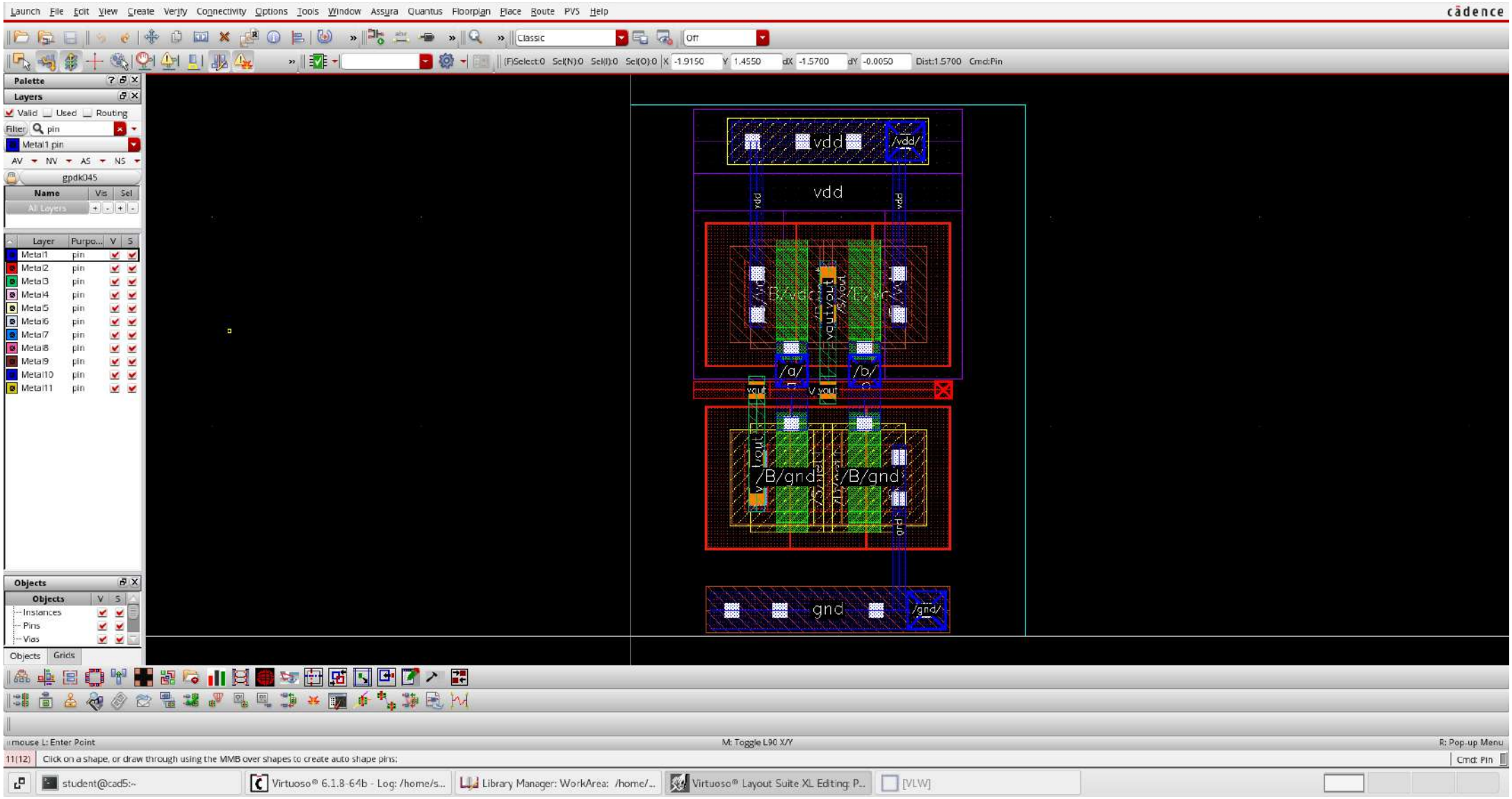

Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Valid Used Routing
Oxide_thk drawing
gpdk045
Name Vis Sel
Layer Pu... V S
Nwell drw
Oxide drw
Oxide_thk drw
Poly drw
Pimp drw
Nhvt drw
Nlvt drw
Nimp drw
Phvt drw
Plvt drw
Nzvt drw
SiProt drw
Cont drw
Metal1 drw
Via1 drw
Metal2 drw
Via2 drw
Metal3 drw
Via3 drw
Metal4 drw
Via4 drw
Metal5 drw
Via5 drw
Metal6 drw
Objects
Instances
Pins
Vias
Objects Grids
vdd
net1
net5
vout
gnd
/a/
/b/
/c/
/d/
/e/
/B/gnd /B/gnd
mouse L: mouseSingleSelectPt()
M: vuiLVSRun()
R: _lxHiMousePopUp()
Cmd:
[student@cad5:~]
[Virtuoso® 6.1.8-64b - Log: /home/...
Library Manager: WorkArea: /home/...
Virtuoso® Layout Suite XL Editing: P...
complimentarycircuit

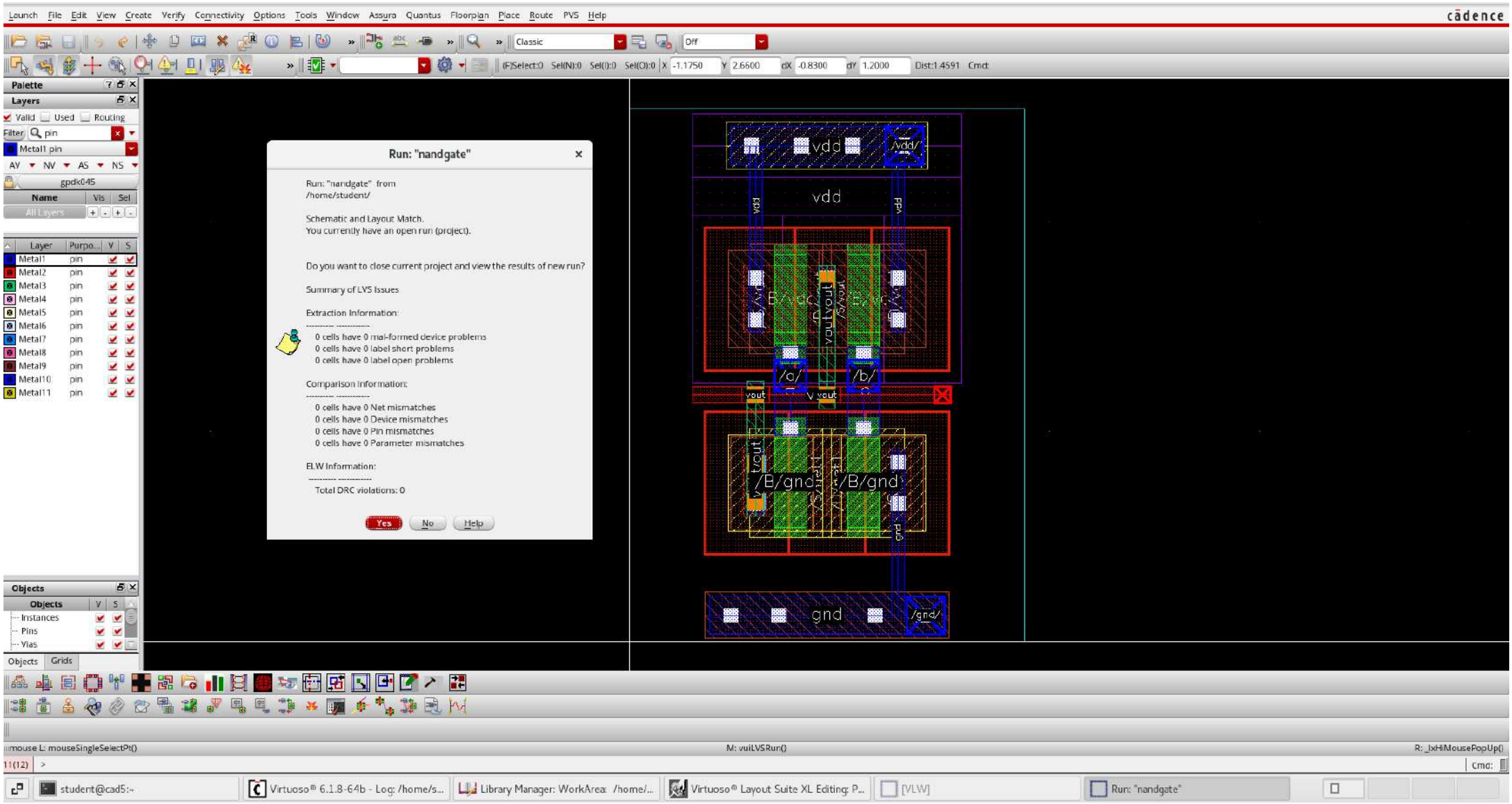

Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Valid Used Routing
Nimp drawing
gpdk045
Name Vis Sel
All Layers
Layer
Nwell
Oxide
Oxide_thk
Poly
Pimp
Nhvt
Nlvt
Nimp
Phvt
Plvt
Nzvt
SiProt
Cont
Metal1
Via1
Metal2
Via2
Metal3
Via3
Metal4
Via4
Metal5
Via5
Metal6
Via6
Metal7
Via7
Metal8
Via8
Objects
vdd
net1
net5
vout
/B/gnd
gnd
PreSel: Inst Name(M6) CellName(pmos2v)
mouse L: mouseSingleSelectPt()
M: hiZoomAbsoluteScale(hiGetCurrentWindow() 0.9)
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /home/s...
Library Manager: WorkArea: /home/...
Virtuoso® Schematic Editor XL Read...
Virtuoso® Layout Suite XL Editing: P...

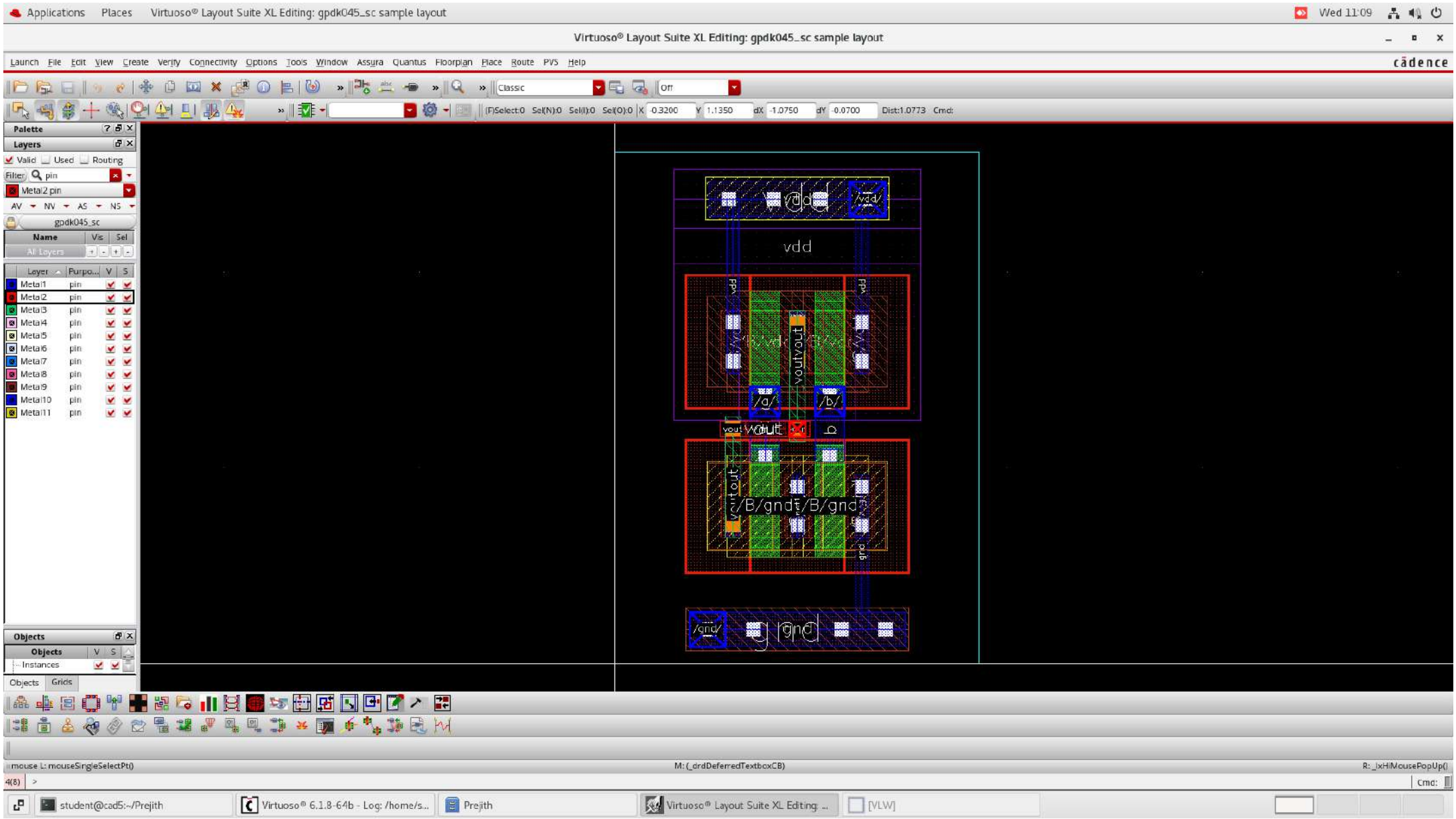

Launch File Edit View Create Check Options Window PVS Help
cadence
Navigator
Schematic
complimentarycircuit
OBJECTS
All
Instances 10
Nets 12
Pins 8
Nets and Pins
GROUPS
Cells
Types
Property Editor
Schemati
Attributes
Library Prejith
Cell complimen...
View schematic
Mode editable
Last Saved Wed Apr 1...
Locked By student@c...
Units inch
vdd
vout
gnd
pmos2v
nmos2v
mouse L: schSingleSelectPt()
M: schZoomFit(1.0 0.9)
R: schHiMousePopUp()
13(15)
Cmd: Sel: 0
[student@cad5:~]
[Virtuoso® 6.1.8-64b - Log: /home/...
Library Manager: WorkArea: /home/...
Virtuoso® Schematic Editor XL Editi...
Virtuoso® Layout Suite XL Editing: P...
complimentarycircuit

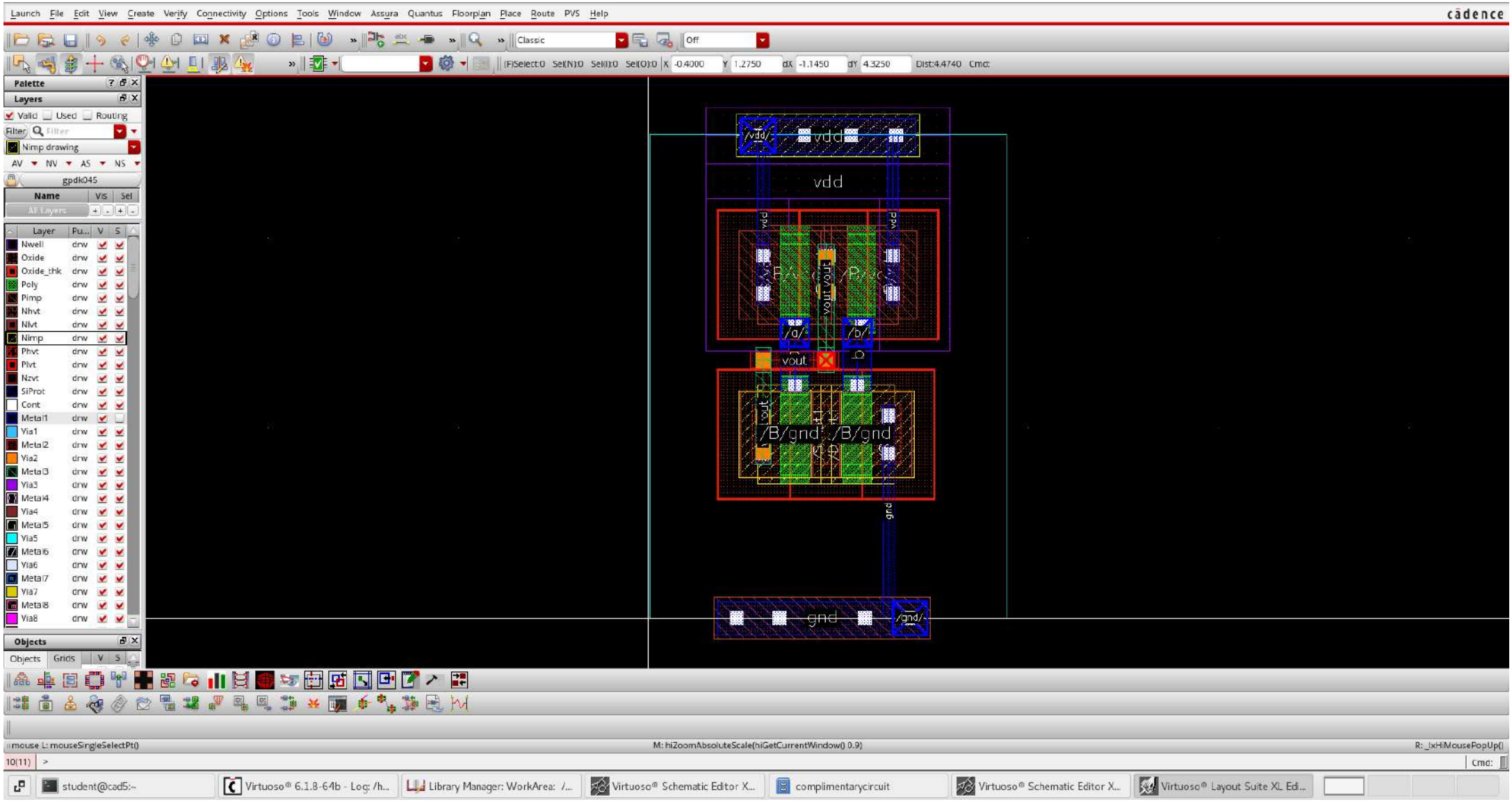

Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Error Layer Window
File View Error-Visibility Show Error by Help
[1] nwell_conn_StampErrorMult
Rules Filter:
AV NV
[1] nwell_conn_StampErrorMult
[1] nwell_conn_StampErrorConnect
[42] POLY.SP.4: Minimum gate space in thick active >= 0.20 um
SignOff
Fixed
Cell Filter:
1 Op_Amp layout
Palette
Layers
Valid Used Routing
Nimp drawing
gpdk045
Name Vis Sel
Layer Purp V S
Nwell drw
Oxide drw
Oxide_thk drw
Poly drw
Pimp drw
Nhvt drw
Nlvt drw
Nimp drw
Phvt drw
Plvt drw
Nzvt drw
SiProt drw
Cont drw
Metal1 drw
Via1 drw
Metal2 drw
Via2 drw
Metal3 drw
Via3 drw
Metal4 drw
Via4 drw
Metal5 drw
Via5 drw
Metal6 drw
Via6 drw
Metal7 drw
Via7 drw
Metal8 drw
Via8 drw
Objects
Objects Grids
vdd
gnd
net1
net2
vout
vin+
vin−
/B/gndB/gndB/gndB/gnd
mouse L: mouseSingleSelectPt()
M: wuiDRCRun()
R: ;IxHiMousePopUp()
Cmd:
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /h...
Library Manager: WorkArea: /...
Virtuoso® Schematic Editor X...
Virtuoso® Layout Suite XL Edi...
[VLW]
Error Layer Window

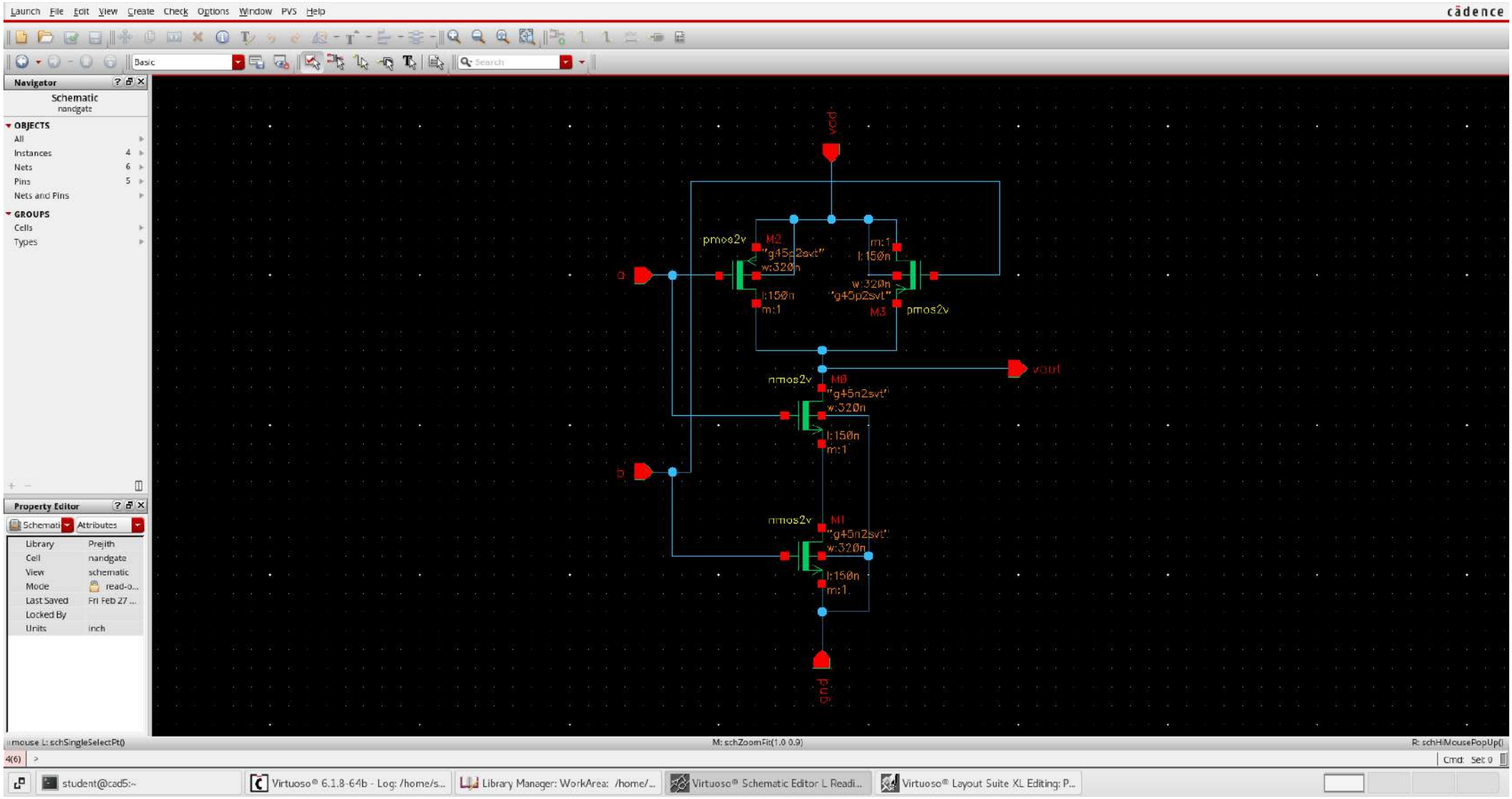

Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Valid Used Routing
Filter pin
Metal1 pin
gpdk045
Name Vis Sel
All Layers
Layer Purpo... V S
Metal1 pin
Metal2 pin
Metal3 pin
Metal4 pin
Metal5 pin
Metal6 pin
Metal7 pin
Metal8 pin
Metal9 pin
Metal10 pin
Metal11 pin
Objects
Instances
Pins
Vias
Objects Grids
No DRC errors found.
Close
vdd
/vdd/
vdd
/a/
/b/
vout
/B/gnd
gnd
/gnd/
mouse L: mouseSingleSelectPt()
M: vuiDRCRun()
R: _lxHiMousePopUp()
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /home/s...
Library Manager: WorkArea: /home/...
Virtuoso® Layout Suite XL Editing: P...
[VLW]

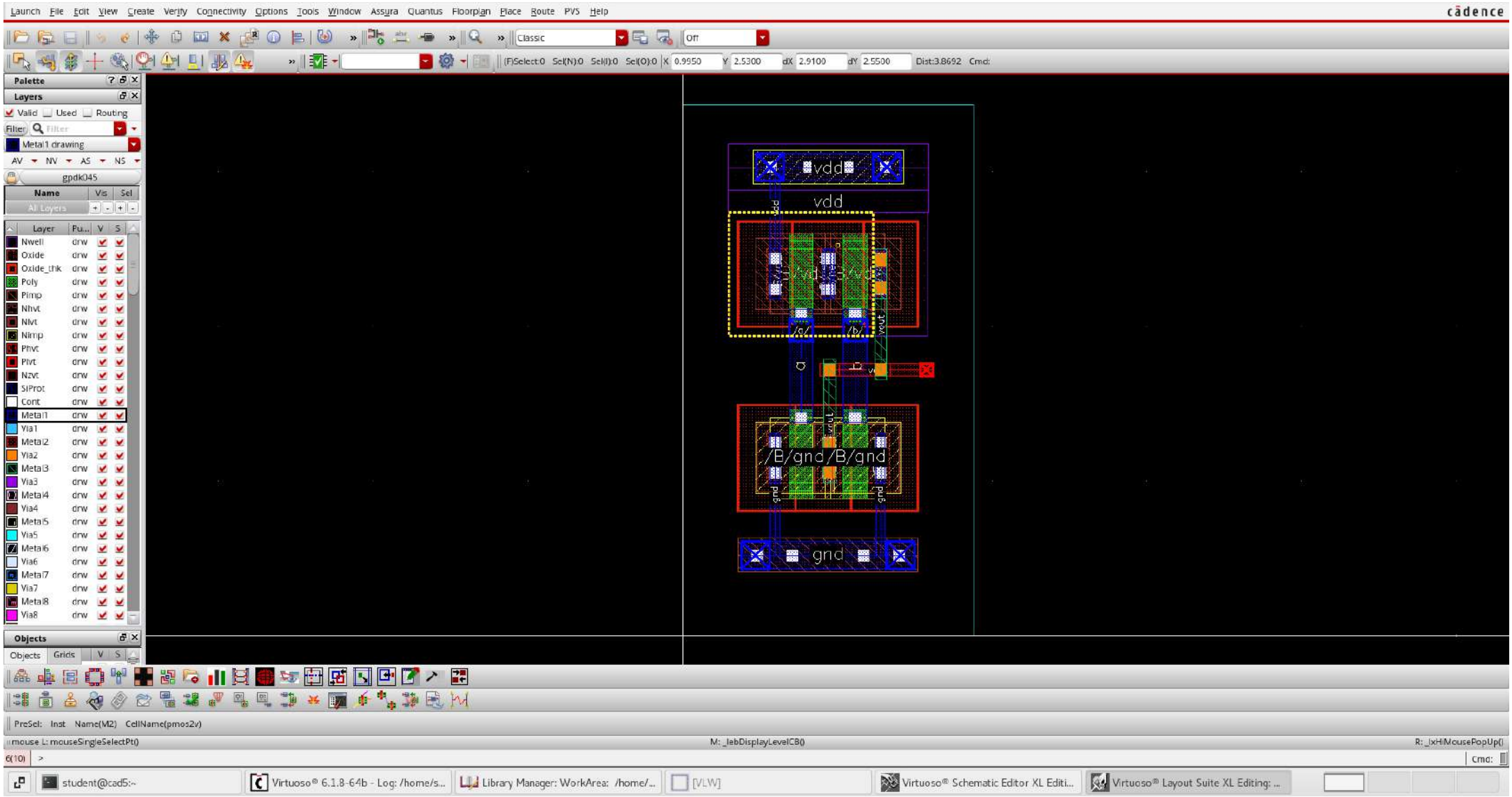

Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Valid Used Routing
Filter pin
Metal1 pin
gpdk045
Name Vis Sel
All Layers
Layer Purpo... V S
Metal1 pin
Metal2 pin
Metal3 pin
Metal4 pin
Metal5 pin
Metal6 pin
Metal7 pin
Metal8 pin
Metal9 pin
Metal10 pin
Metal11 pin
Objects
Objects V S
Instances
Pins
Vias
Objects Grids
vdd
/vdd/
vdd
/a/
/b/
vout
/B/gnd
/B/gnd
gnd
/gnd/
mouse L: Enter Point
M: Toggle L90 X/Y
R: Pop-up Menu
11(12) Click on a shape, or draw through using the MMB over shapes to create auto shape pins:
Cmd: Pin
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /home/s...
Library Manager: WorkArea: /home/...
Virtuoso® Layout Suite XL Editing: P...
[VLW]

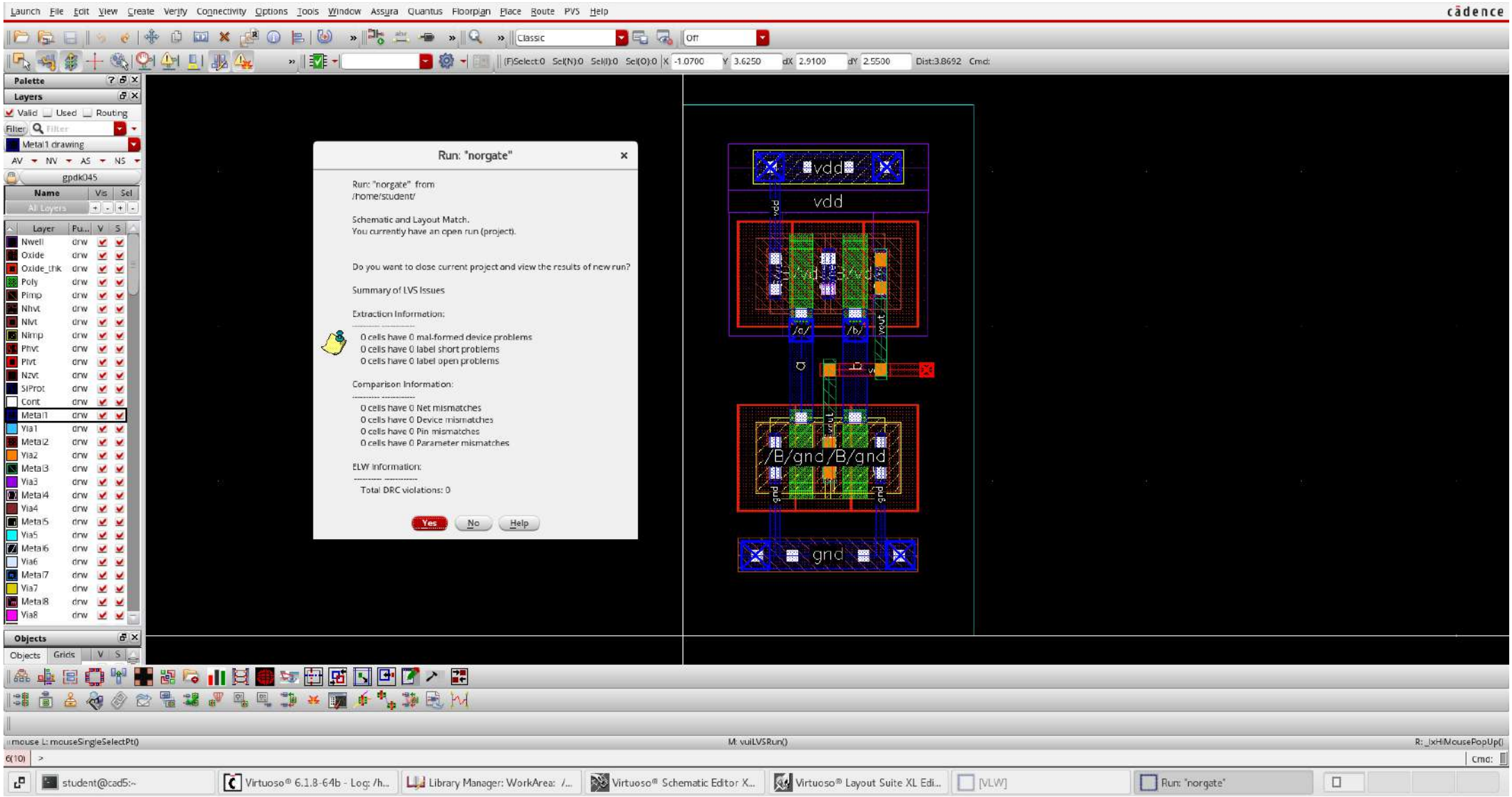

Run: "nandgate"
Run: "nandgate" from
/home/student/
Schematic and Layout Match.
You currently have an open run (project).
Do you want to close current project and view the results of new run?
Summary of LVS Issues
Extraction Information:
0 cells have 0 mal-formed device problems
0 cells have 0 label short problems
0 cells have 0 label open problems
Comparison Information:
0 cells have 0 Net mismatches
0 cells have 0 Device mismatches
0 cells have 0 Pin mismatches
0 cells have 0 Parameter mismatches
ELW Information:
Total DRC violations: 0
Yes
No
Help
vdd
/vdd/
gnd
/gnd/
/a/
/b/
vout
/B/gnd
Palette
Layers
Objects
Instances
Pins
Vias
Objects
Grids
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /home/s...
Library Manager: WorkArea: /home/...
Virtuoso® Layout Suite XL Editing: P...
[VLW]
Run: "nandgate"

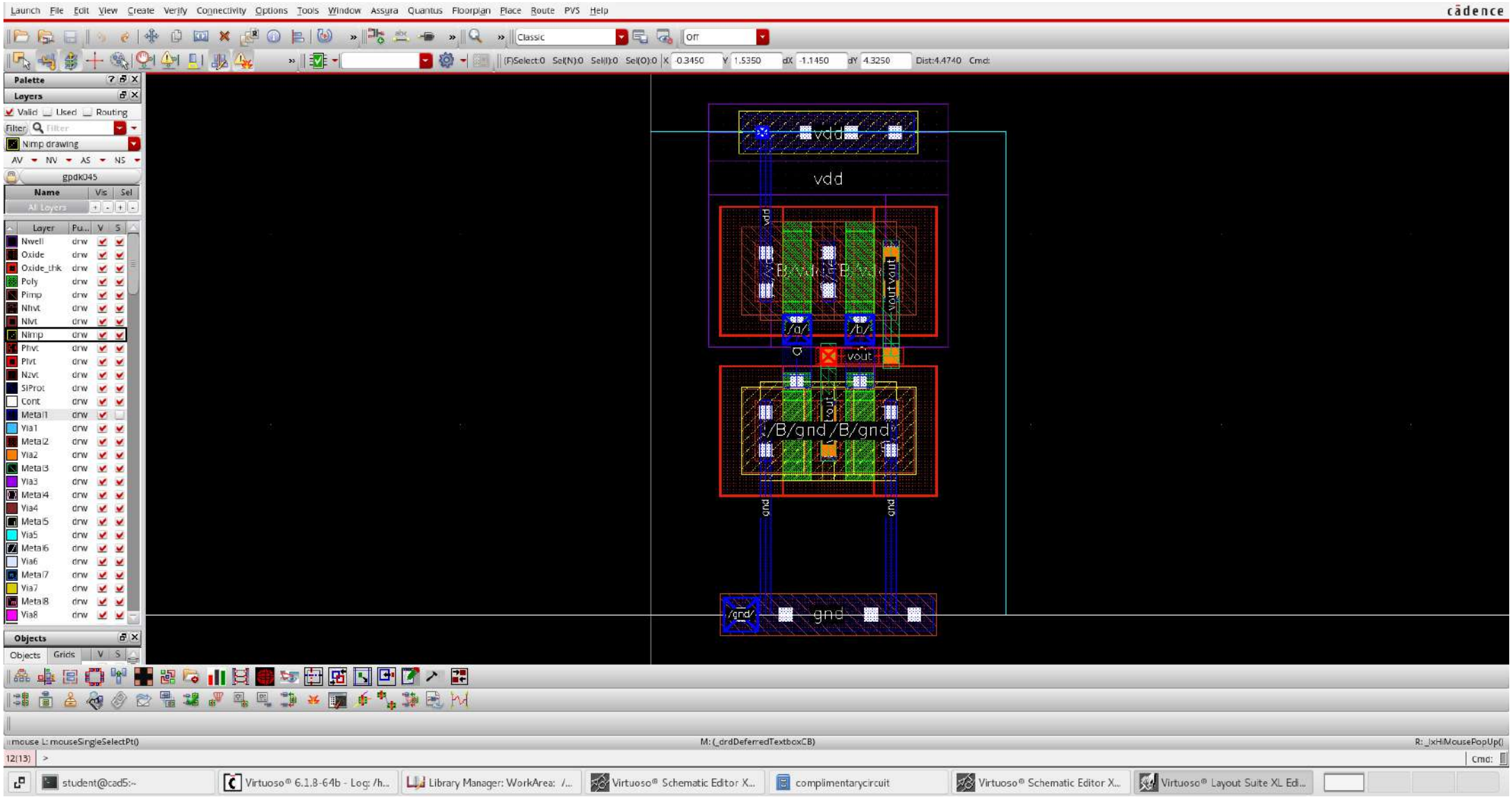

Applications Places Virtuoso® Layout Suite XL Editing: gpdk045_sc sample layout
Wed 11:09
Virtuoso® Layout Suite XL Editing: gpdk045_sc sample layout
Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Classic
Off
(F)Select:0 Sel(N):0 Sel(I):0 Sel(O):0 X -0.3200 Y 1.1350 dX -1.0750 dY -0.0700 Dist:1.0773 Cmd:
Palette
Layers
Valid Used Routing
Filter pin
Metal2 pin
AV NV AS NS
gpdk045_sc
Name Vis Sel
All Layers
Layer Purpo... V S
Metal1 pin
Metal2 pin
Metal3 pin
Metal4 pin
Metal5 pin
Metal6 pin
Metal7 pin
Metal8 pin
Metal9 pin
Metal10 pin
Metal11 pin
Objects
Objects V S
Instances
Objects Grids
vdd
/vdd/
vdd
/a/
/b/
vout
/B/gnd
/gnd/
gnd
mouse L: mouseSingleSelectPt()
M: (_drdDeferredTextboxCB)
R: _lxHiMousePopUp()
cmd:
student@cad5:~/Prejith
Virtuoso® 6.1.8-64b - Log: /home/s...
Prejith
Virtuoso® Layout Suite XL Editing: ...
[VLW]

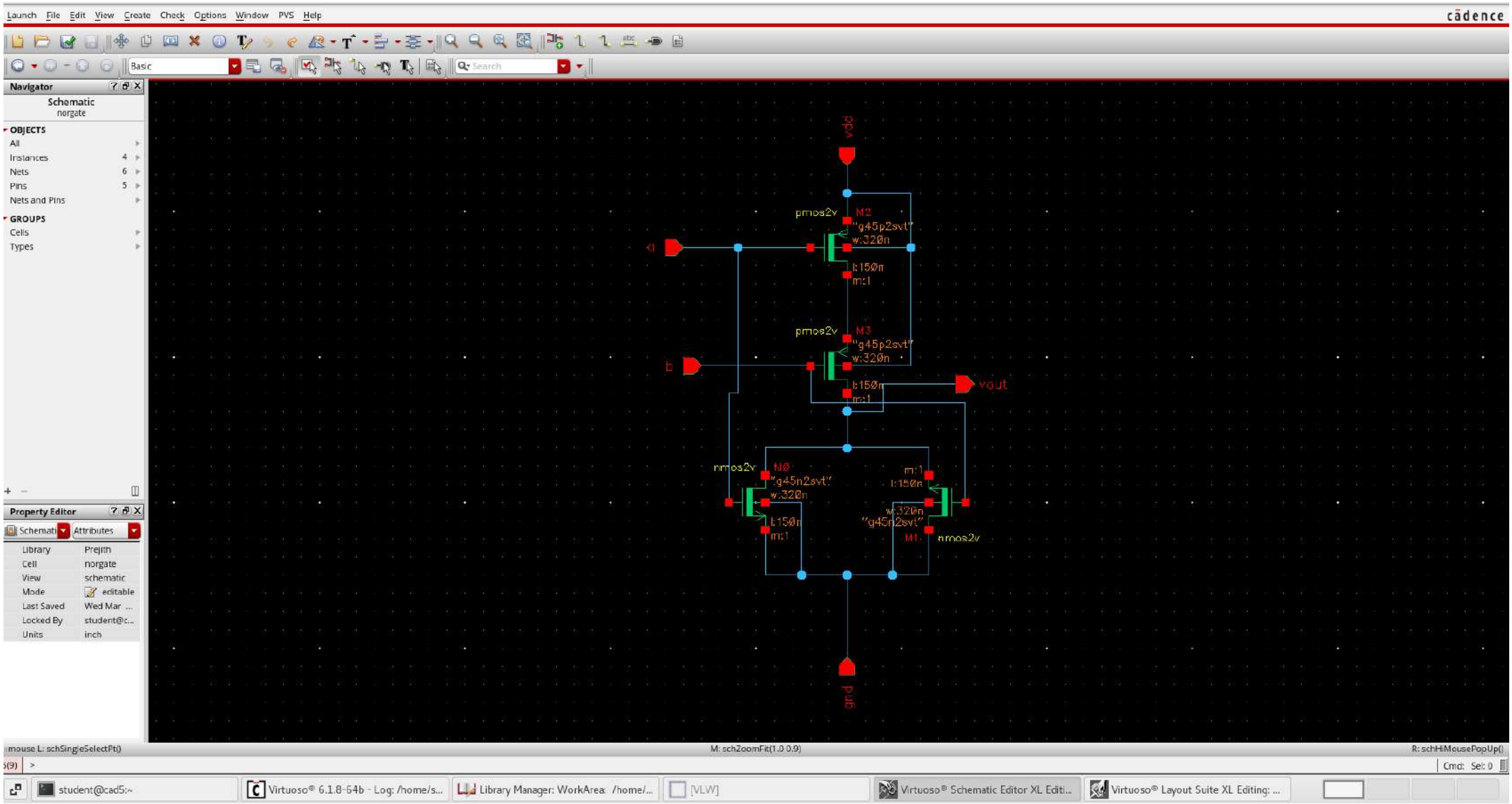

Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Valid Used Routing
Nimp drawing
gpdk045
Objects
vdd
/vdd/
vout
/a/
/b/
/B/gnd
gnd
/gnd/
mouse L: mouseSingleSelectPt()
M: hiZoomAbsoluteScale(hiGetCurrentWindow() 0.9)
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /h...
Library Manager: WorkArea: /...
Virtuoso® Schematic Editor X...
complimentarycircuit
Virtuoso® Layout Suite XL Edi...

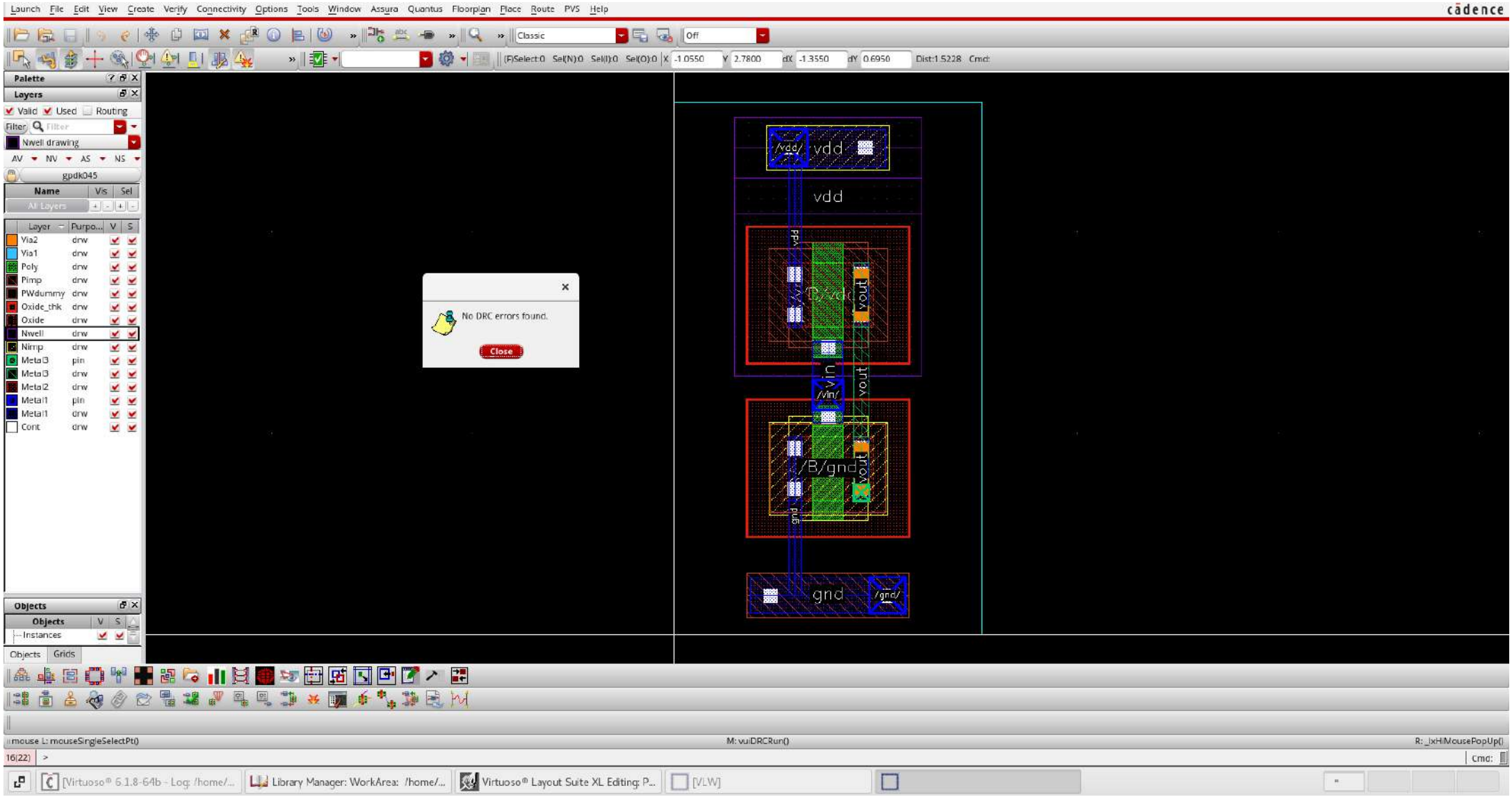

Launch File Edit View Create Check Options Window PVS Help
cadence
Navigator
Schematic
nandgate
OBJECTS
All
Instances 4
Nets 6
Pins 5
Nets and Pins
GROUPS
Cells
Types
Property Editor
Schemati
Attributes
Library Prejith
Cell nandgate
View schematic
Mode read-o...
Last Saved Fri Feb 27 ...
Locked By
Units inch
vdd
pmos2v
M2
"g45p2svt"
w:320n
l:150n
m:1
m:1
l:150n
w:320n
"g45p2svt"
M3
pmos2v
a
vout
nmos2v
M0
"g45n2svt"
w:320n
l:150n
m:1
b
nmos2v
M1
"g45n2svt"
w:320n
l:150n
m:1
gnd
mouse L: schSingleSelectPt()
M: schZoomFit(1.0 0.9)
R: schHiMousePopUp()
Cmd: Set: 0
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /home/s...
Library Manager: WorkArea: /home/...
Virtuoso® Schematic Editor L Readi...
Virtuoso® Layout Suite XL Editing: P...

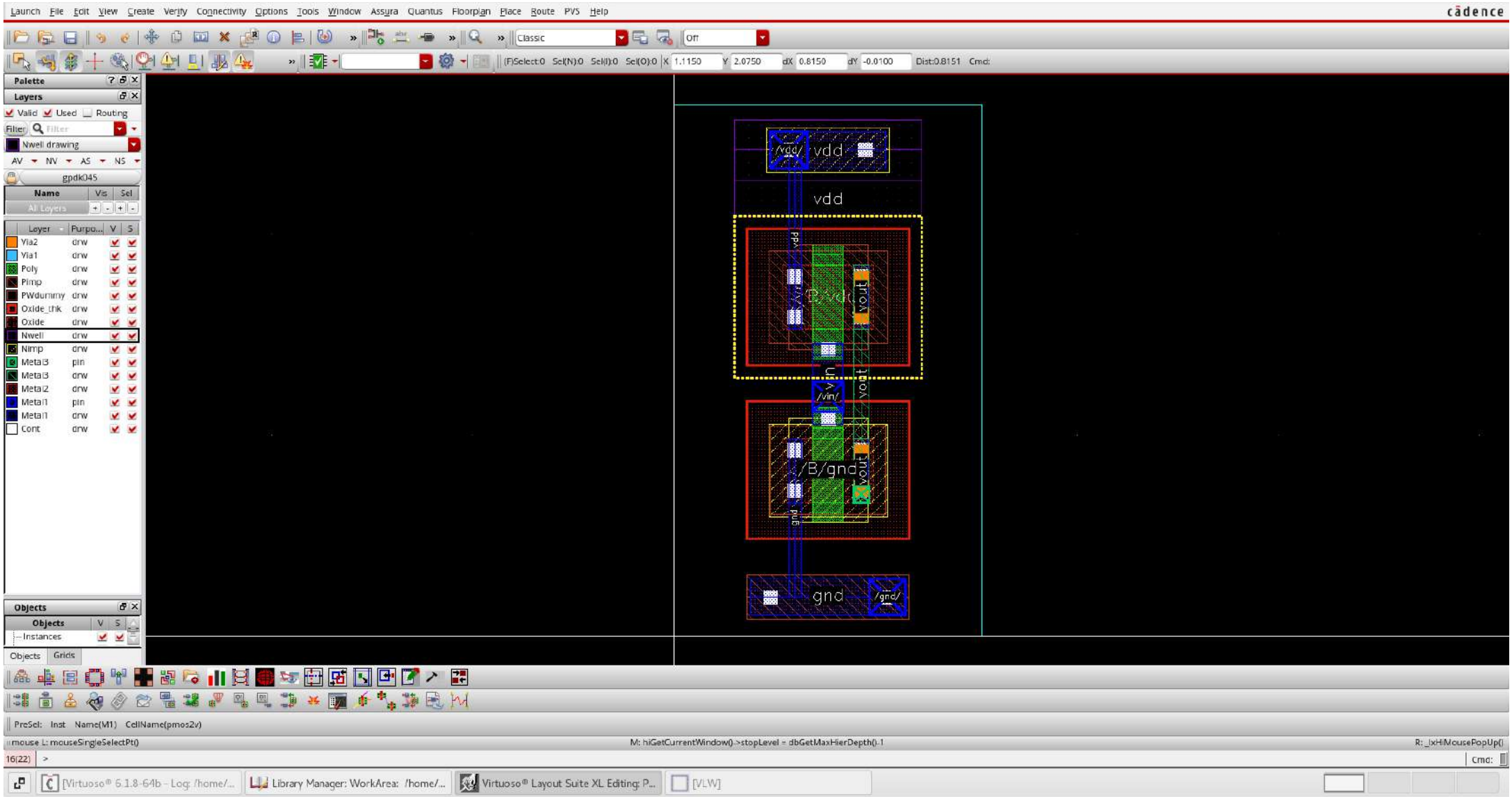

Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Valid Used Routing
Metal1 drawing
gpdk045
Name Vis Sel
All Layers
Layer Pu... V S
Nwell drw
Oxide drw
Oxide_thk drw
Poly drw
Pimp drw
Nhvt drw
Nlvt drw
Nimp drw
Phvt drw
Plvt drw
Nzvt drw
SIProt drw
Cont drw
Metal1 drw
Via1 drw
Metal2 drw
Via2 drw
Metal3 drw
Via3 drw
Metal4 drw
Via4 drw
Metal5 drw
Via5 drw
Metal6 drw
Via6 drw
Metal7 drw
Via7 drw
Metal8 drw
Via8 drw
Objects
Objects Grids V S
vdd
vdd
/a/
/b/
/B/gnd/B/gnd
gnd
PreSel: Inst Name(M2) CellName(pmos2v)
mouse L: mouseSingleSelectPt()
M: _lebDisplayLevelCB()
R: _lxHiMousePopUp()
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /home/s...
Library Manager: WorkArea: /home/...
[VLW]
Virtuoso® Schematic Editor XL Editi...
Virtuoso® Layout Suite XL Editing: ...

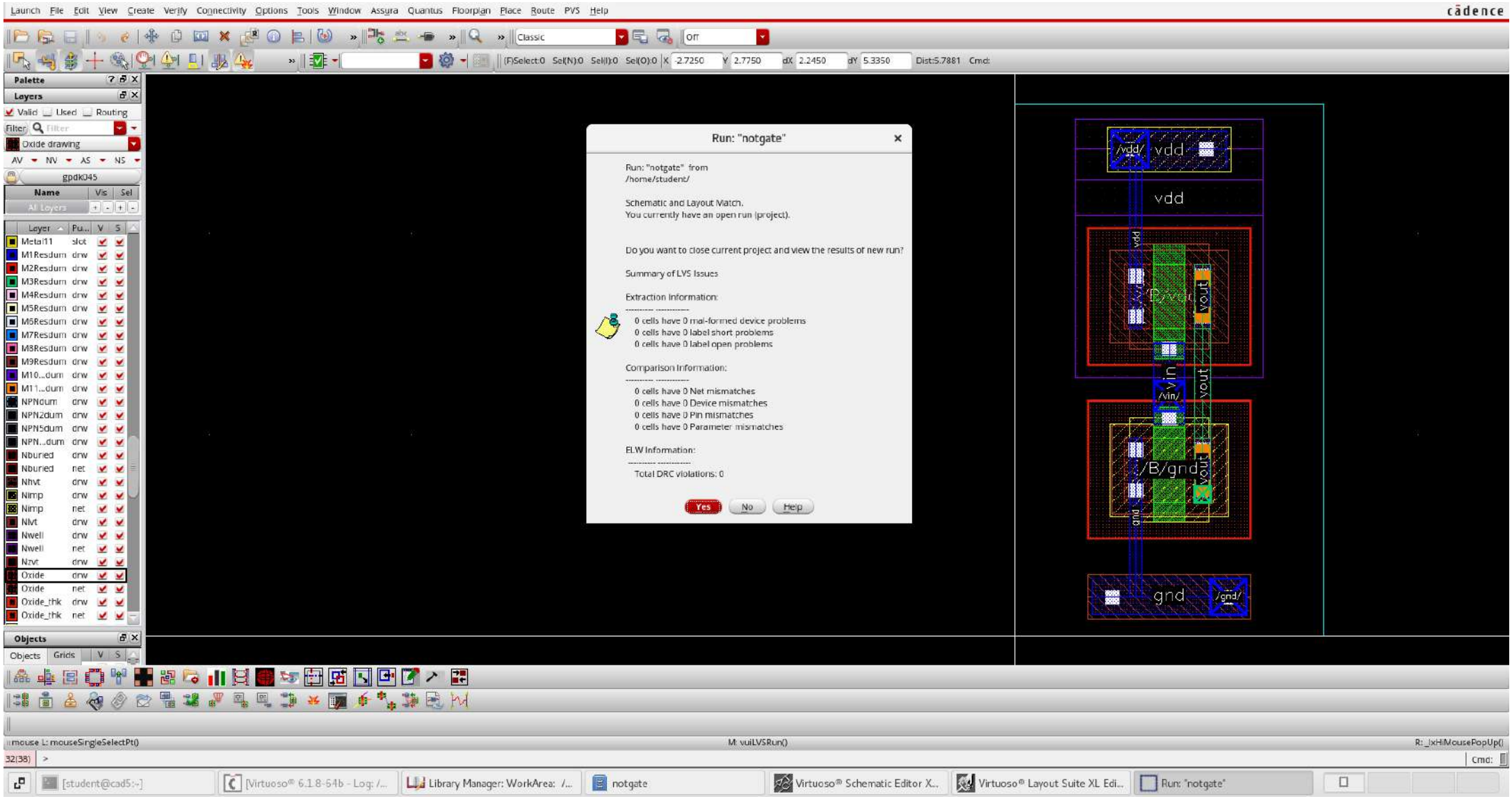

Run: "norgate"
Run: "norgate" from
/home/student/
Schematic and Layout Match.
You currently have an open run (project).
Do you want to close current project and view the results of new run?
Summary of LVS Issues
Extraction Information:
0 cells have 0 mal-formed device problems
0 cells have 0 label short problems
0 cells have 0 label open problems
Comparison Information:
0 cells have 0 Net mismatches
0 cells have 0 Device mismatches
0 cells have 0 Pin mismatches
0 cells have 0 Parameter mismatches
ELW Information:
Total DRC violations: 0
Yes
No
Help
vdd
gnd
/a/
/b/
/vout
/B/gnd/B/gnd

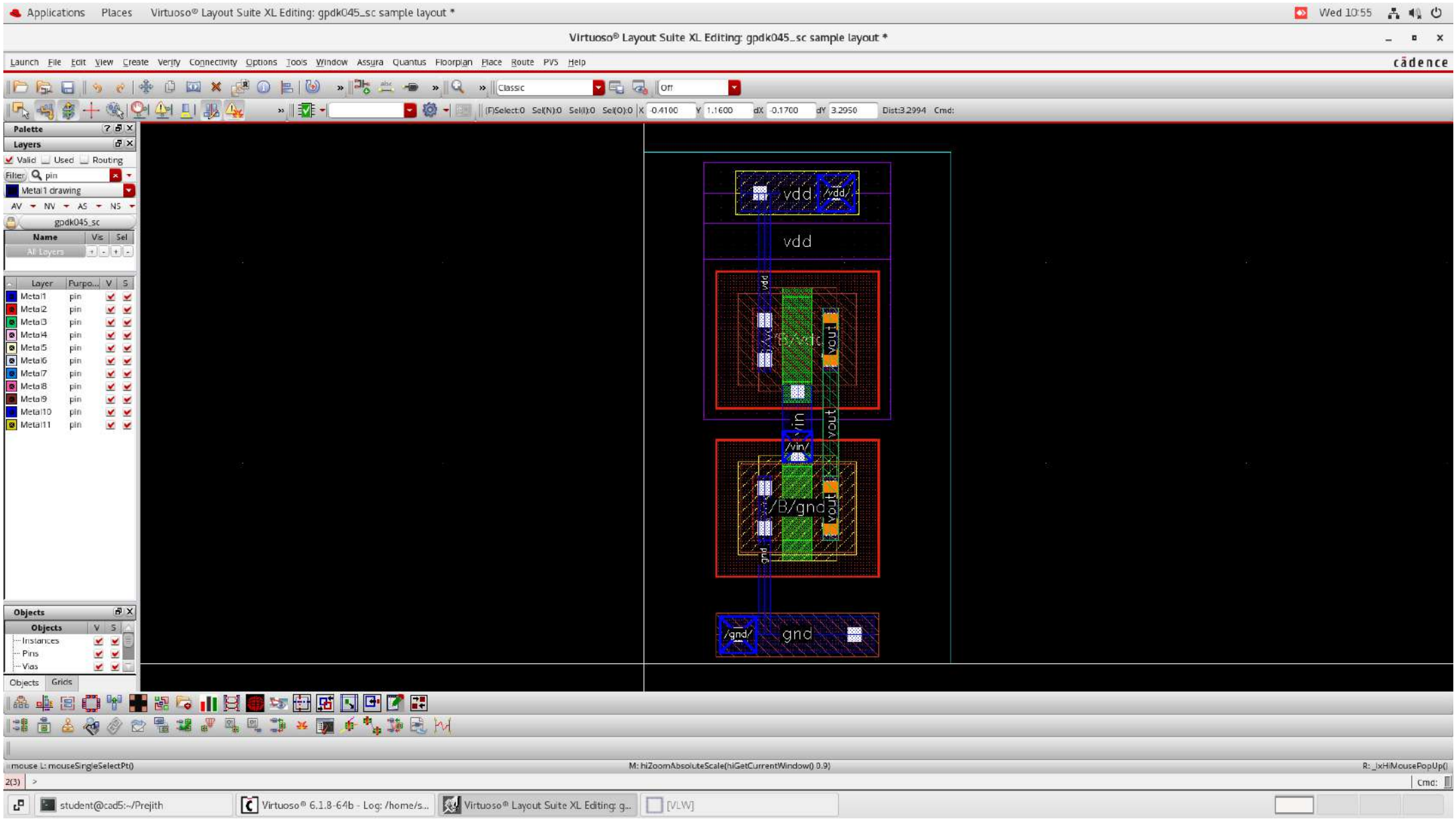

Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Valid Used Routing
Nimp drawing
gpdk045
Name Vis Sel
All Layers
Layer Pu... V S
Nwell drw
Oxide drw
Oxide_thk drw
Poly drw
Pimp drw
Nhvt drw
Nlvt drw
Nimp drw
Phvt drw
Plvt drw
Nzvt drw
SiProt drw
Cont drw
Metal1 drw
Via1 drw
Metal2 drw
Via2 drw
Metal3 drw
Via3 drw
Metal4 drw
Via4 drw
Metal5 drw
Via5 drw
Metal6 drw
Via6 drw
Metal7 drw
Via7 drw
Metal8 drw
Via8 drw
Objects
Objects Grids V S
vdd
vdd
vout
/a/
/b/
vout
/B/gnd/B/gnd
gnd
gnd
/gnd/
gnd
mouse L: mouseSingleSelectPt()
M: (_drdDeferredTextboxCB)
R: _lxHiMousePopUp()
Cmd:
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /h...
Library Manager: WorkArea: /...
Virtuoso® Schematic Editor X...
complimentarycircuit
Virtuoso® Schematic Editor X...
Virtuoso® Layout Suite XL Edi...

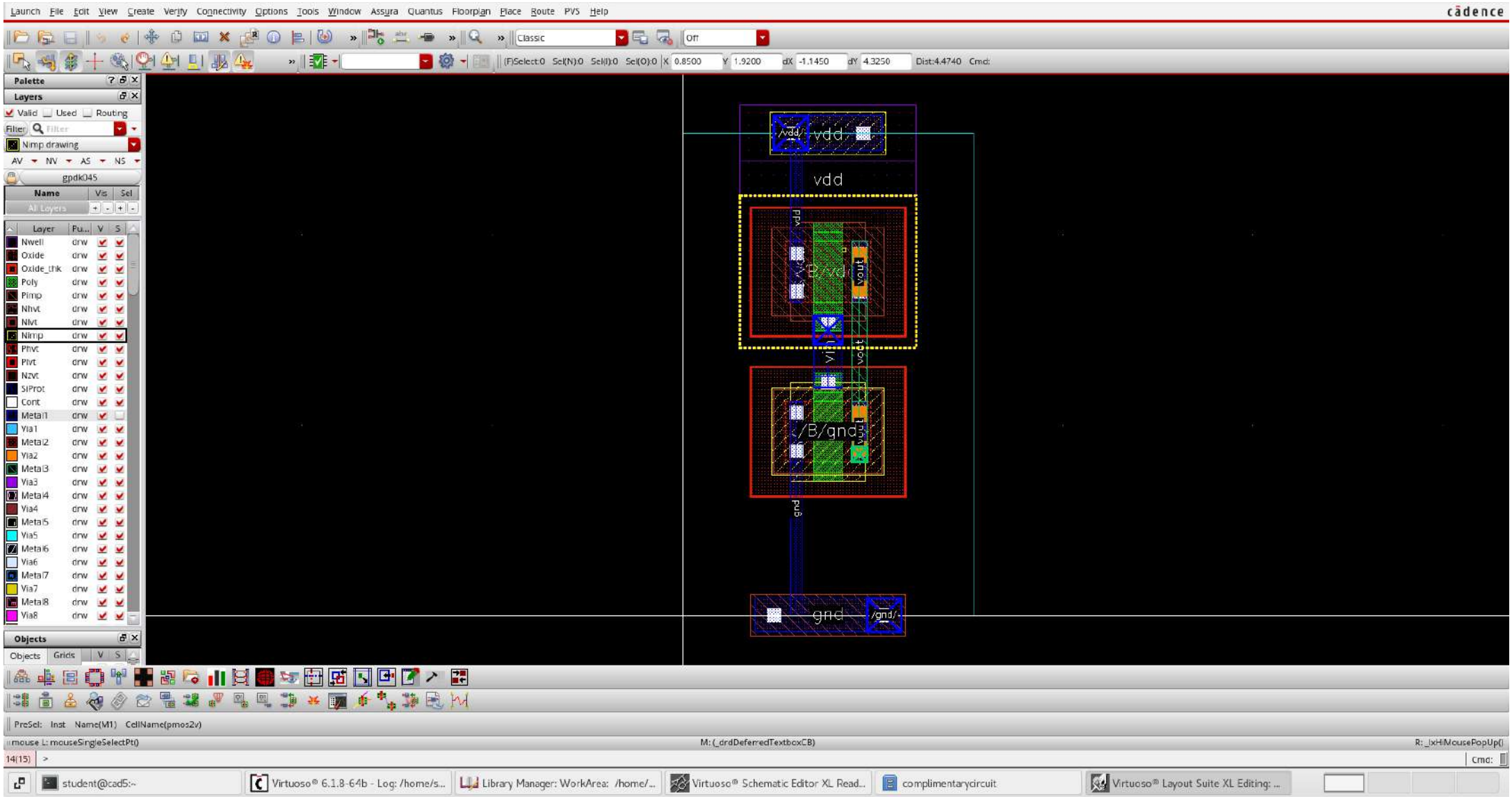

Launch File Edit View Create Check Options Window PVS Help
cadence
Navigator
Schematic
norgate
OBJECTS
All
Instances 4
Nets 6
Pins 5
Nets and Pins
GROUPS
Cells
Types
Property Editor
Schemati
Attributes
Library Prejith
Cell norgate
View schematic
Mode editable
Last Saved Wed Mar ...
Locked By student@c...
Units inch
vdd
pmos2v
M2
"g45p2svt"
w:320n
l:150n
m:1
a
pmos2v
M3
"g45p2svt"
w:320n
l:150n
m:1
b
vout
nmos2v
M0
"g45n2svt"
w:320n
l:150n
m:1
m:1
l:150n
w:320n
"g45n2svt"
M1
nmos2v
gnd
mouse L: schSingleSelectPt()
M: schZoomFit(1.0 0.9)
R: schHiMousePopUp()
Cmd: Sel: 0
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /home/s...
Library Manager: WorkArea: /home/...
[VLW]
Virtuoso® Schematic Editor XL Editi...
Virtuoso® Layout Suite XL Editing: ...

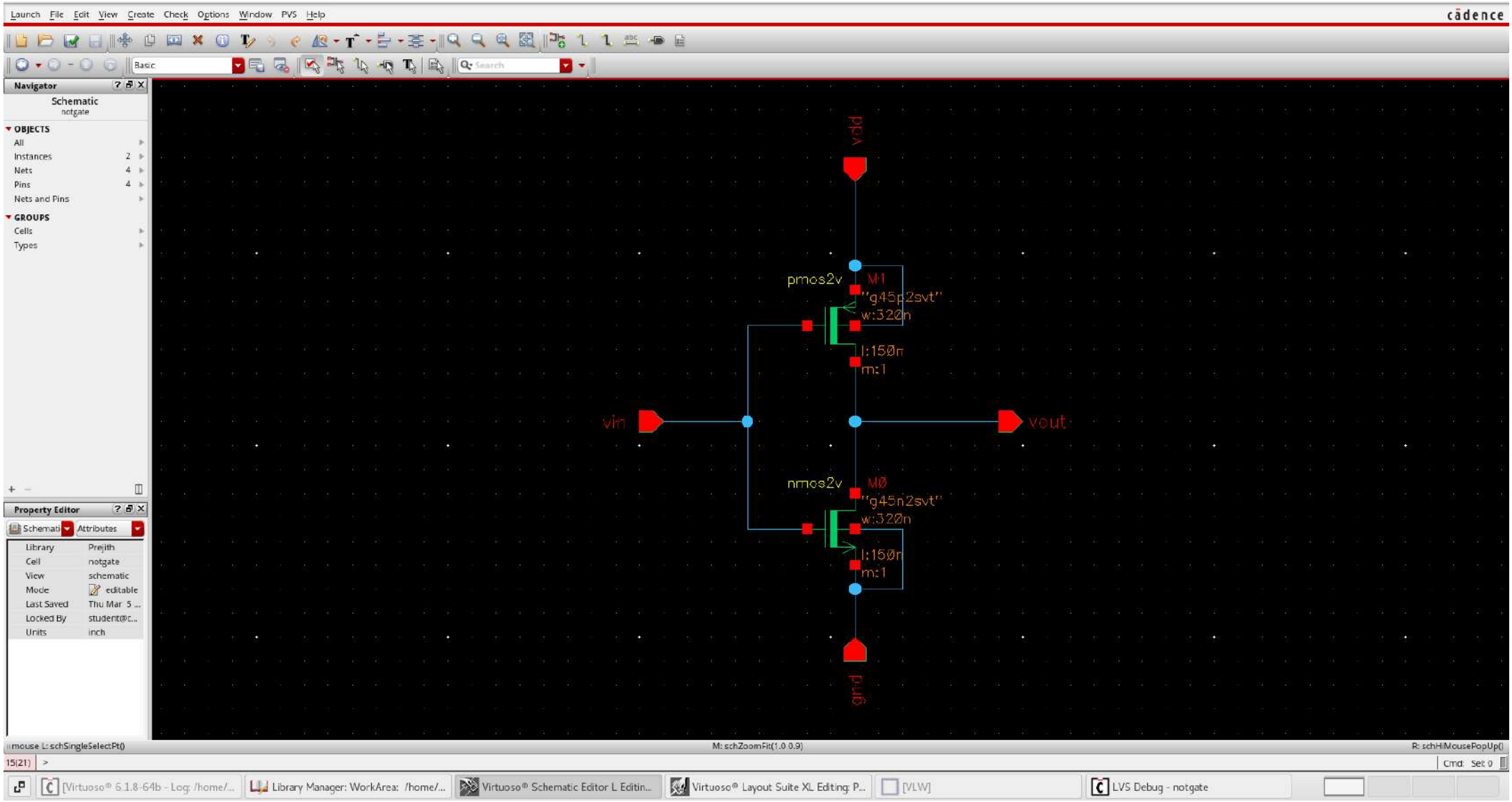

Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Valid Used Routing
Nwell drawing
gpdk045
Name Vis Sel
All Layers
Layer Purpo... V S
Via2 drw
Via1 drw
Poly drw
Pimp drw
PWdummy drw
Oxide_thk drw
Oxide drw
Nwell drw
Nimp drw
Metal3 pin
Metal3 drw
Metal2 drw
Metal1 pin
Metal1 drw
Cont drw
Objects
Objects V S
Instances
Objects Grids
No DRC errors found.
Close
vdd
/vdd/
vdd
/B/vdd
vout
vin
/vin/
vout
/B/gnd
vout
gnd
/gnd/
mouse L: mouseSingleSelectPt()
M: vuiDRCRun()
R: _lxHiMousePopUp()
cmd:
[Virtuoso® 6.1.8-64b - Log: /home/...
Library Manager: WorkArea: /home/...
Virtuoso® Layout Suite XL Editing: P...
[VLW]

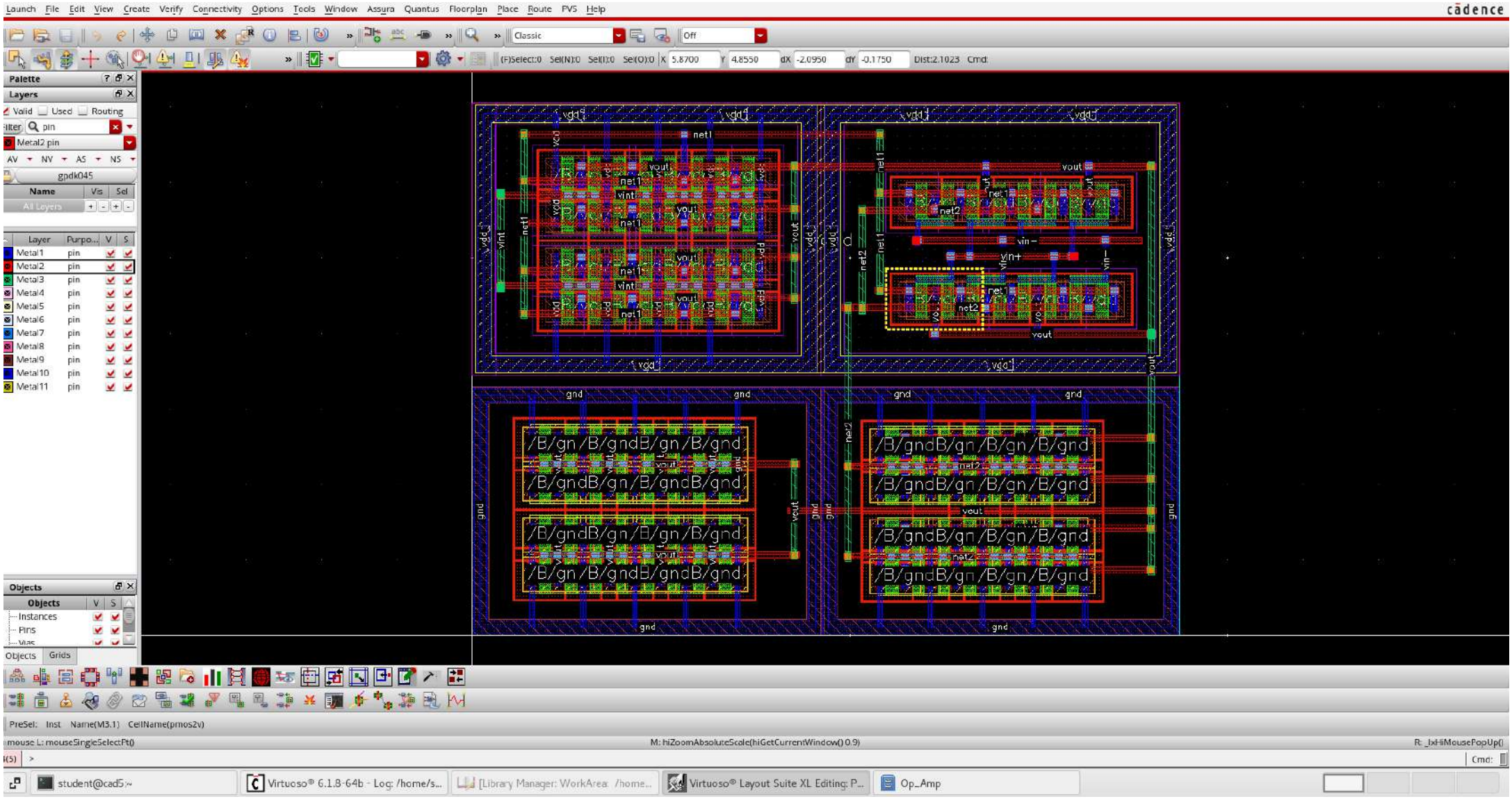

Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Valid Used Routing
Nwell drawing
gpdk045
Name Vis Sel
All Layers
Layer Purpo... V S
Via2 drw
Via1 drw
Poly drw
Pimp drw
PWdummy drw
Oxide_thk drw
Oxide drw
Nwell drw
Nimp drw
Metal3 pin
Metal3 drw
Metal2 drw
Metal1 pin
Metal1 drw
Cont drw
Objects
Objects V S
Instances
Objects Grids
/vdd/
vdd
vdd
/B/vdd
vout
/vin/
vout
/B/gnd
vout
gnd
/gnd/
PreSel: Inst Name(M1) CellName(pmos2v)
mouse L: mouseSingleSelectPt()
M: hiGetCurrentWindow().stopLevel = dbGetMaxHierDepth()-1
R: _lxHiMousePopUp()
Cmd:
[Virtuoso® 6.1.8-64b - Log: /home/...
Library Manager: WorkArea: /home/...
Virtuoso® Layout Suite XL Editing: P...
[VLW]

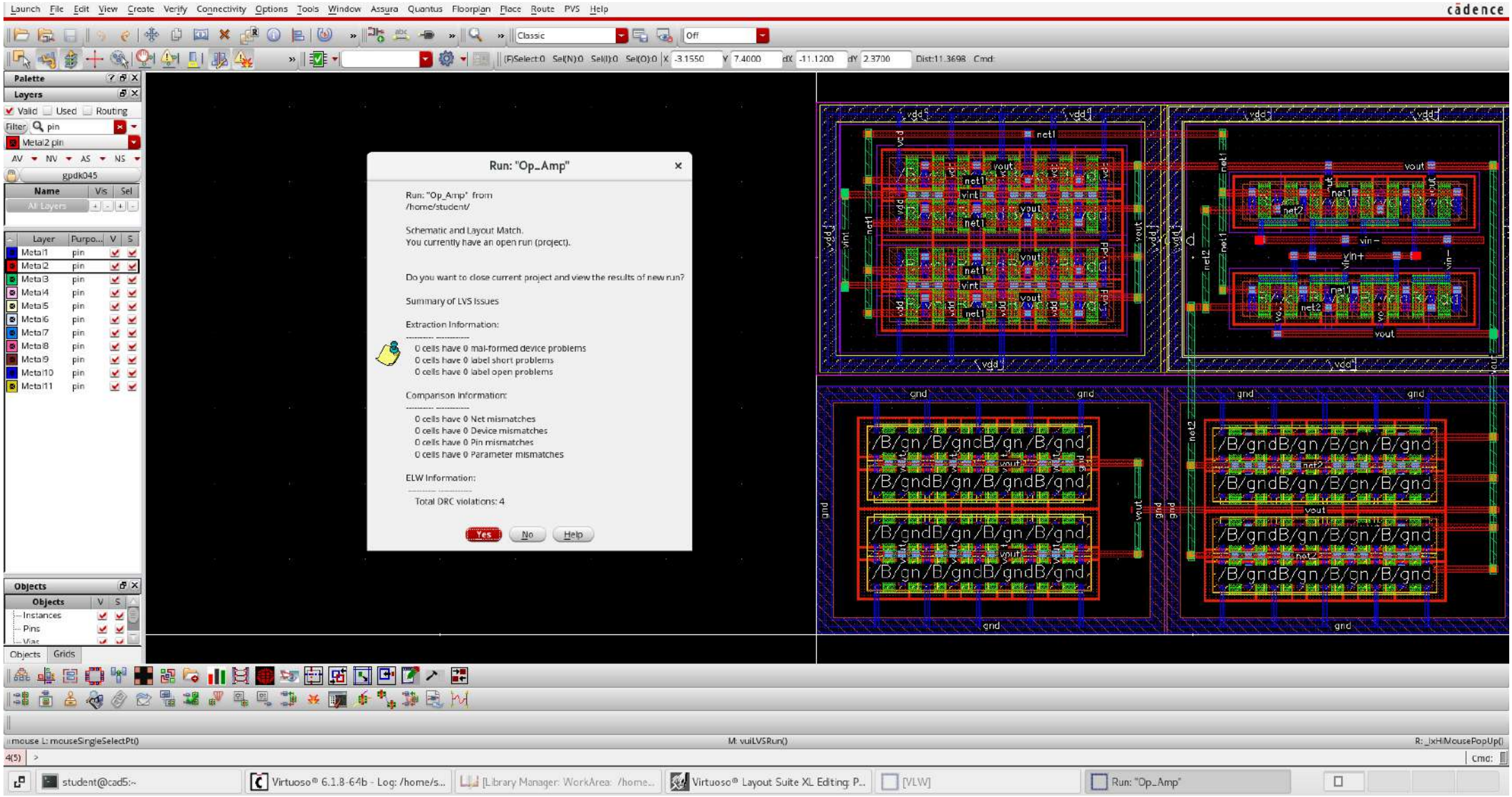

Run: "notgate"
Run: "notgate" from
/home/student/
Schematic and Layout Match.
You currently have an open run (project).
Do you want to close current project and view the results of new run?
Summary of LVS Issues
Extraction Information:
0 cells have 0 mal-formed device problems
0 cells have 0 label short problems
0 cells have 0 label open problems
Comparison Information:
0 cells have 0 Net mismatches
0 cells have 0 Device mismatches
0 cells have 0 Pin mismatches
0 cells have 0 Parameter mismatches
ELW Information:
Total DRC violations: 0
Yes
No
Help
vdd
/vdd/
vin
/vin/
vout
/B/gnd
gnd
/gnd/

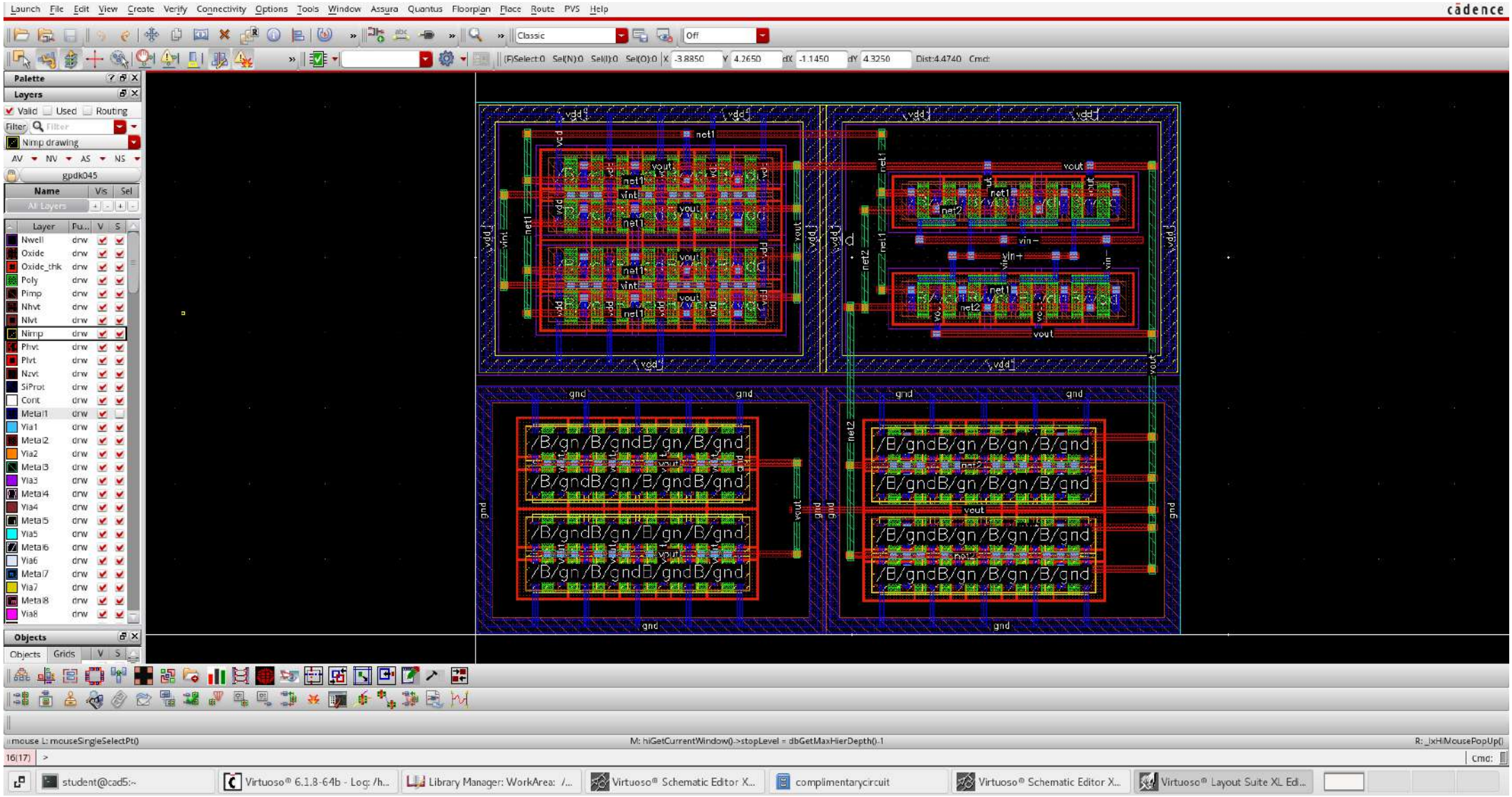

Applications Places Virtuoso® Layout Suite XL Editing: gpdk045_sc sample layout *
Wed 10:55
Virtuoso® Layout Suite XL Editing: gpdk045_sc sample layout *
Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Classic
Off
(F)Select:0 Sel(N):0 Sel(I):0 Sel(O):0 X -0.4100 Y 1.1600 dX -0.1700 dY 3.2950 Dist:3.2994 Cmd:
Palette
Layers
Valid Used Routing
Filter pin
Metal1 drawing
AV NV AS NS
gpdk045_sc
Name Vis Sel
All Layers
Layer Purpo... V S
Metal1 pin
Metal2 pin
Metal3 pin
Metal4 pin
Metal5 pin
Metal6 pin
Metal7 pin
Metal8 pin
Metal9 pin
Metal10 pin
Metal11 pin
Objects
Objects V S
Instances
Pins
Vias
Objects Grids
vdd
/vdd/
vdd
vdd
/A/vdd
vout
/vin/
vin
vout
/B/gnd
vout
gnd
/gnd/
gnd
mouse L: mouseSingleSelectPt()
M: hiZoomAbsoluteScale(hiGetCurrentWindow() 0.9)
R: _ixHiMousePopUp()
2(3) >
Cmd:
student@cad5:~/Prejith
Virtuoso® 6.1.8-64b - Log: /home/s...
Virtuoso® Layout Suite XL Editing: g...
[VLW]

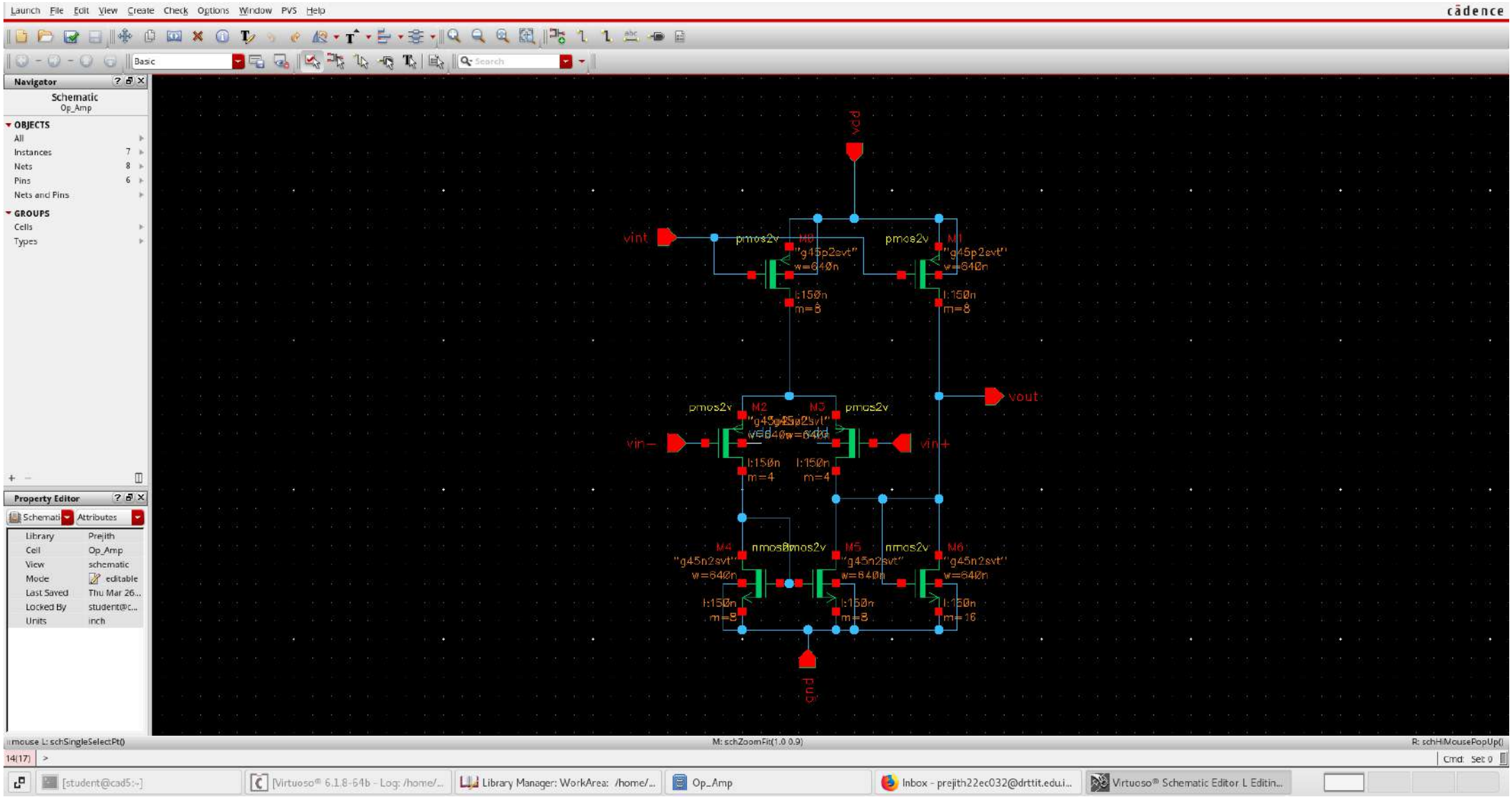

Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Valid Used Routing
Nimp drawing
gpdk045
Name Vis Sel
All Layers
Layer Pu... V S
Nwell drw
Oxide drw
Oxide_thk drw
Poly drw
Pimp drw
Nhvt drw
Nlvt drw
Nimp drw
Phvt drw
Plvt drw
Nzvt drw
SIProt drw
Cont drw
Metal1 drw
Via1 drw
Metal2 drw
Via2 drw
Metal3 drw
Via3 drw
Metal4 drw
Via4 drw
Metal5 drw
Via5 drw
Metal6 drw
Via6 drw
Metal7 drw
Via7 drw
Metal8 drw
Via8 drw
Objects
Objects Grids V S
vdd
/vdd/
/B/vdd
/B/gnd
gnd
/gnd/
PreSel: Inst Name(M1) CellName(pmos2v)
mouse L: mouseSingleSelectPt()
M: (_drdDeferredTextboxCB)
R: _lxHiMousePopUp()
Cmd:
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /home/s...
Library Manager: WorkArea: /home/...
Virtuoso® Schematic Editor XL Read...
complimentarycircuit
Virtuoso® Layout Suite XL Editing: ...

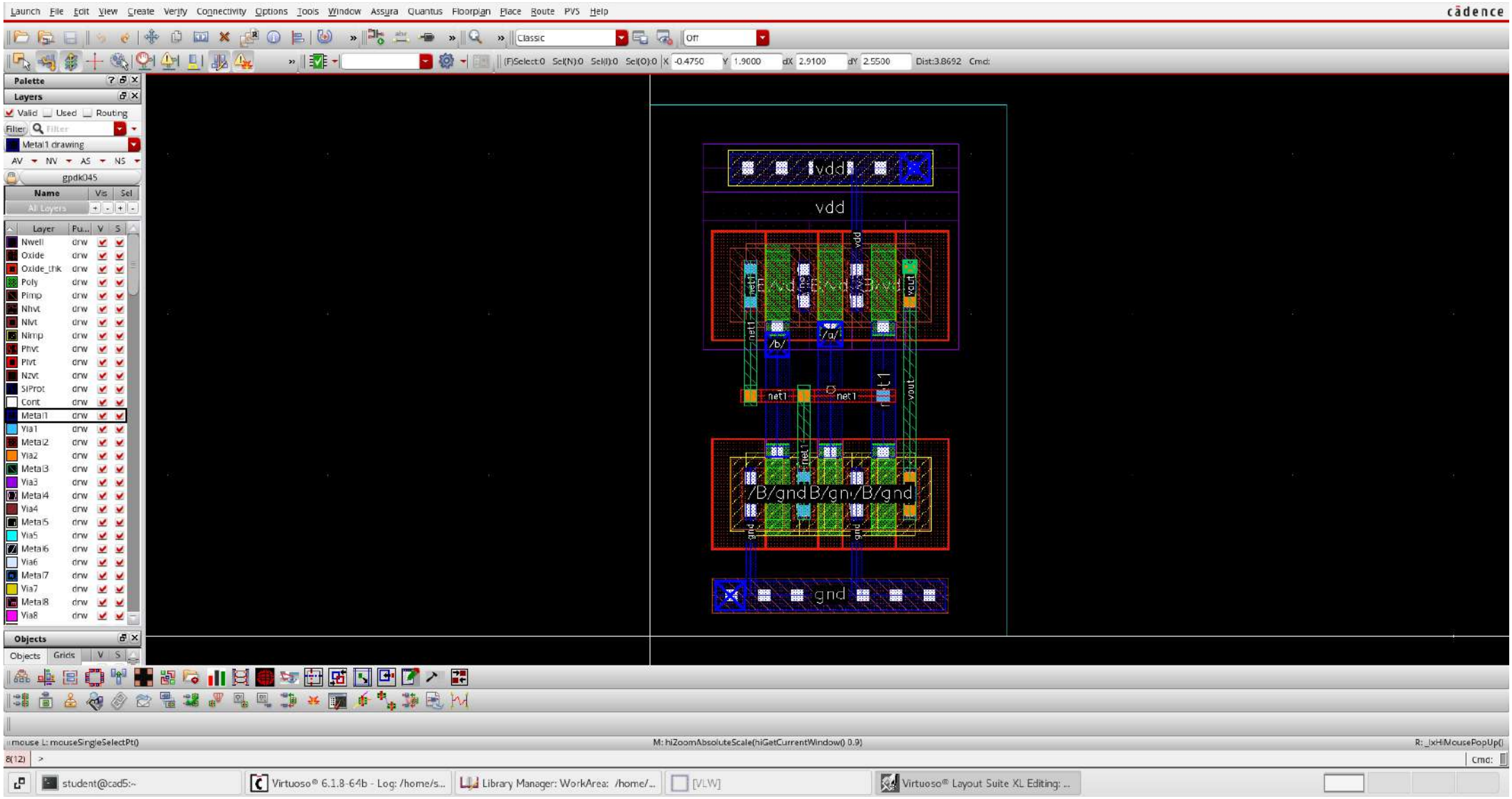

Launch File Edit View Create Check Options Window PVS Help
cadence
Navigator
Schematic
notgate
OBJECTS
All
Instances 2
Nets 4
Pins 4
Nets and Pins
GROUPS
Cells
Types
Property Editor
Schemati
Attributes
Library Prejith
Cell notgate
View schematic
Mode editable
Last Saved Thu Mar 5 ...
Locked By student@c...
Units inch
vdd
pmos2v
M1
"g45p2svt"
w:320n
l:150n
m:1
vin
vout
nmos2v
M0
"g45n2svt"
w:320n
l:150n
m:1
gnd
mouse L: schSingleSelectPt()
M: schZoomFit(1.0 0.9)
R: schHiMousePopUp()
15(21)
Cmd: Sel: 0
[Virtuoso® 6.1.8-64b - Log: /home/...
Library Manager: WorkArea: /home/...
Virtuoso® Schematic Editor L Editin...
Virtuoso® Layout Suite XL Editing: P...
[VLW]
LVS Debug - notgate


Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Metal2 pin
gpdk045
Objects
vdd
net1
vout
vint
net2
vin−
vin+
gnd
/B/gn/B/gndB/gn/B/gnd
/B/gndB/gn/B/gndB/gnd
/B/gndB/gn/B/gn/B/gnd
/B/gn/B/gndB/gndB/gnd
/B/gndB/gn/B/gn/B/gnd
/B/gndB/gn/B/gn/B/gnd
/B/gndB/gn/B/gn/B/gnd
PreSel: Inst Name(M3.1) CellName(pmos2v)
mouse L: mouseSingleSelectPt()
M: hiZoomAbsoluteScale(hiGetCurrentWindow() 0.9)
R: _lxHiMousePopUp()
Cmd:
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /home/s...
[Library Manager: WorkArea: /home...
Virtuoso® Layout Suite XL Editing: P...
Op_Amp


Run: "Op_Amp"
Run: "Op_Amp" from
/home/student/
Schematic and Layout Match.
You currently have an open run (project).
Do you want to close current project and view the results of new run?
Summary of LVS Issues
Extraction Information:
0 cells have 0 mal-formed device problems
0 cells have 0 label short problems
0 cells have 0 label open problems
Comparison Information:
0 cells have 0 Net mismatches
0 cells have 0 Device mismatches
0 cells have 0 Pin mismatches
0 cells have 0 Parameter mismatches
ELW Information:
Total DRC violations: 4
Yes
No
Help


Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Valid Used Routing
Nimp drawing
gpdk045
Name Vis Sel
All Layers
Layer Pu... V S
Nwell drw
Oxide drw
Oxide_thk drw
Poly drw
Pimp drw
Nhvt drw
Nlvt drw
Nimp drw
Phvt drw
Plvt drw
Nzvt drw
SiProt drw
Cont drw
Metal1 drw
Via1 drw
Metal2 drw
Via2 drw
Metal3 drw
Via3 drw
Metal4 drw
Via4 drw
Metal5 drw
Via5 drw
Metal6 drw
Via6 drw
Metal7 drw
Via7 drw
Metal8 drw
Via8 drw
Objects
Objects Grids
vdd
gnd
net1
net2
vout
vin−
vin+
vint
/B/gn/B/gndB/gn/B/gnd
/B/gndB/gn/B/gn/B/gnd
/B/gndB/gn/B/gndB/gnd
/B/gn/B/gndB/gndB/gnd
mouse L: mouseSingleSelectPt()
M: hiGetCurrentWindow()->stopLevel = dbGetMaxHierDepth()-1
R: _lxHiMousePopUp()
cmd:
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /h...
Library Manager: WorkArea: /...
Virtuoso® Schematic Editor X...
complimentarycircuit
Virtuoso® Schematic Editor X...
Virtuoso® Layout Suite XL Edi...


Launch File Edit View Create Check Options Window PVS Help
cadence
Navigator
Schematic
Op_Amp
OBJECTS
All
Instances 7
Nets 8
Pins 6
Nets and Pins
GROUPS
Cells
Types
Property Editor
Schemati
Attributes
Library Prejith
Cell Op_Amp
View schematic
Mode editable
Last Saved Thu Mar 26...
Locked By student@c...
Units inch
vdd
vint
pmos2v
M0
"g45p2svt"
w=640n
l:150n
m=8
M1
vout
M2
M3
vin−
vin+
l:150n
m=4
M4
M5
M6
nmos2v
"g45n2svt"
w=640n
m=16
gnd
mouse L: schSingleSelectPt()
M: schZoomFit(1.0 0.9)
R: schHiMousePopUp()
14(17)
Cmd: Set: 0
[student@cad5:~]
[Virtuoso® 6.1.8-64b - Log: /home/...
Library Manager: WorkArea: /home/...
Op_Amp
Inbox - prejith22ec032@drttit.edu.i...
Virtuoso® Schematic Editor L Editin...


Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Valid Used Routing
Metal1 drawing
gpdk045
Name Vis Sel
All Layers
Layer Pu... V S
Nwell drw
Oxide drw
Oxide_thk drw
Poly drw
Pimp drw
Nhvt drw
Nlvt drw
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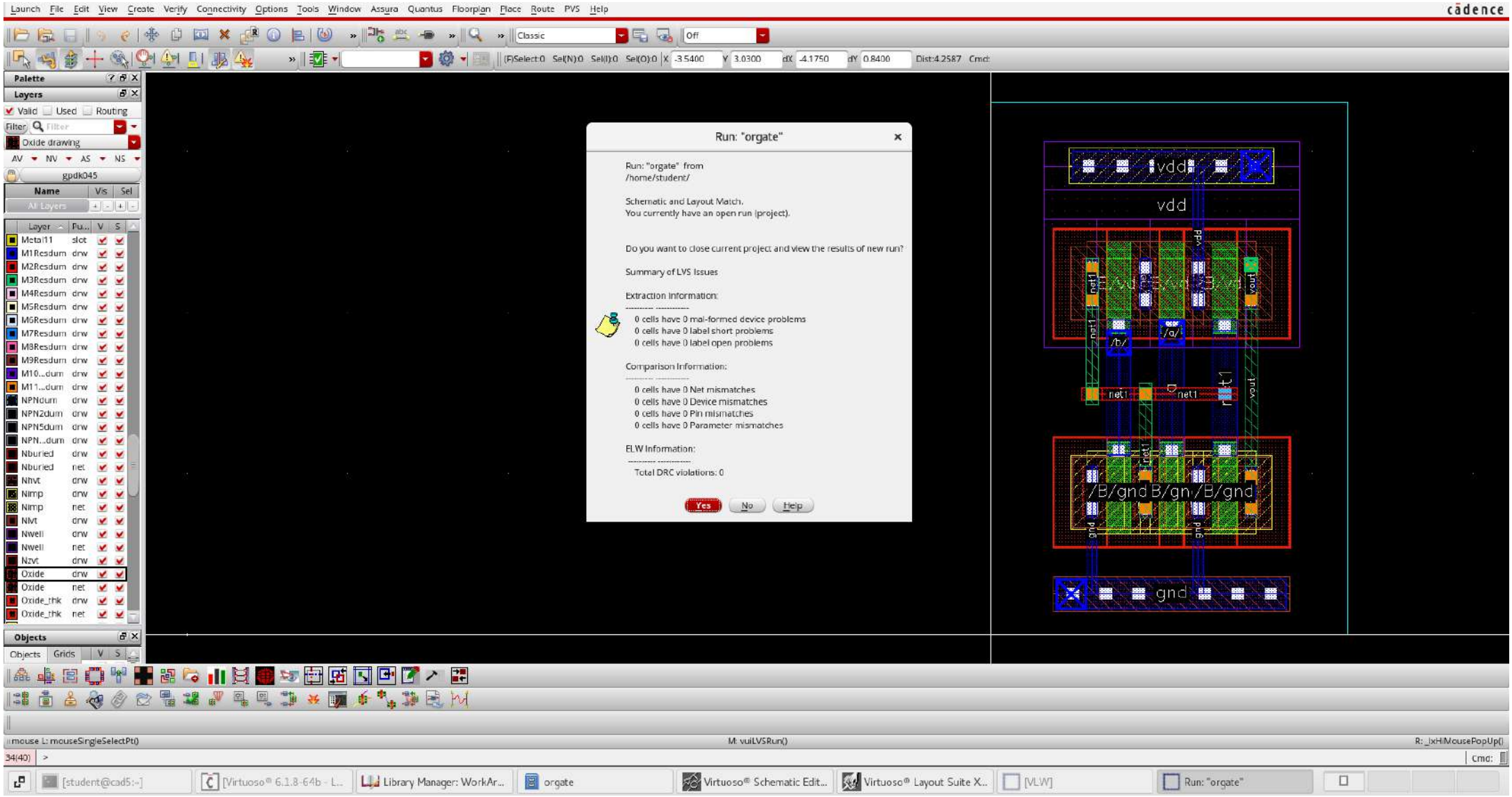
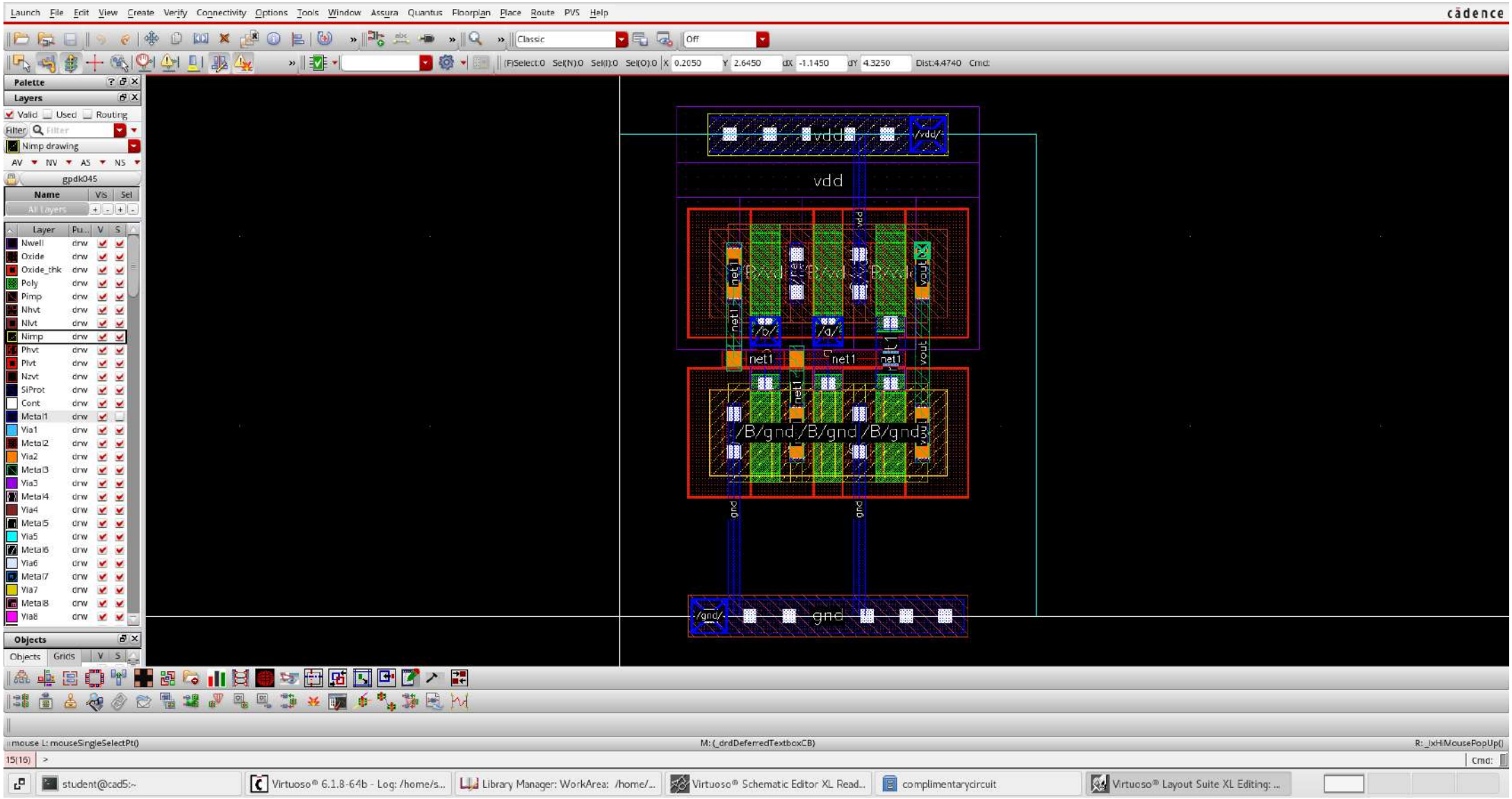
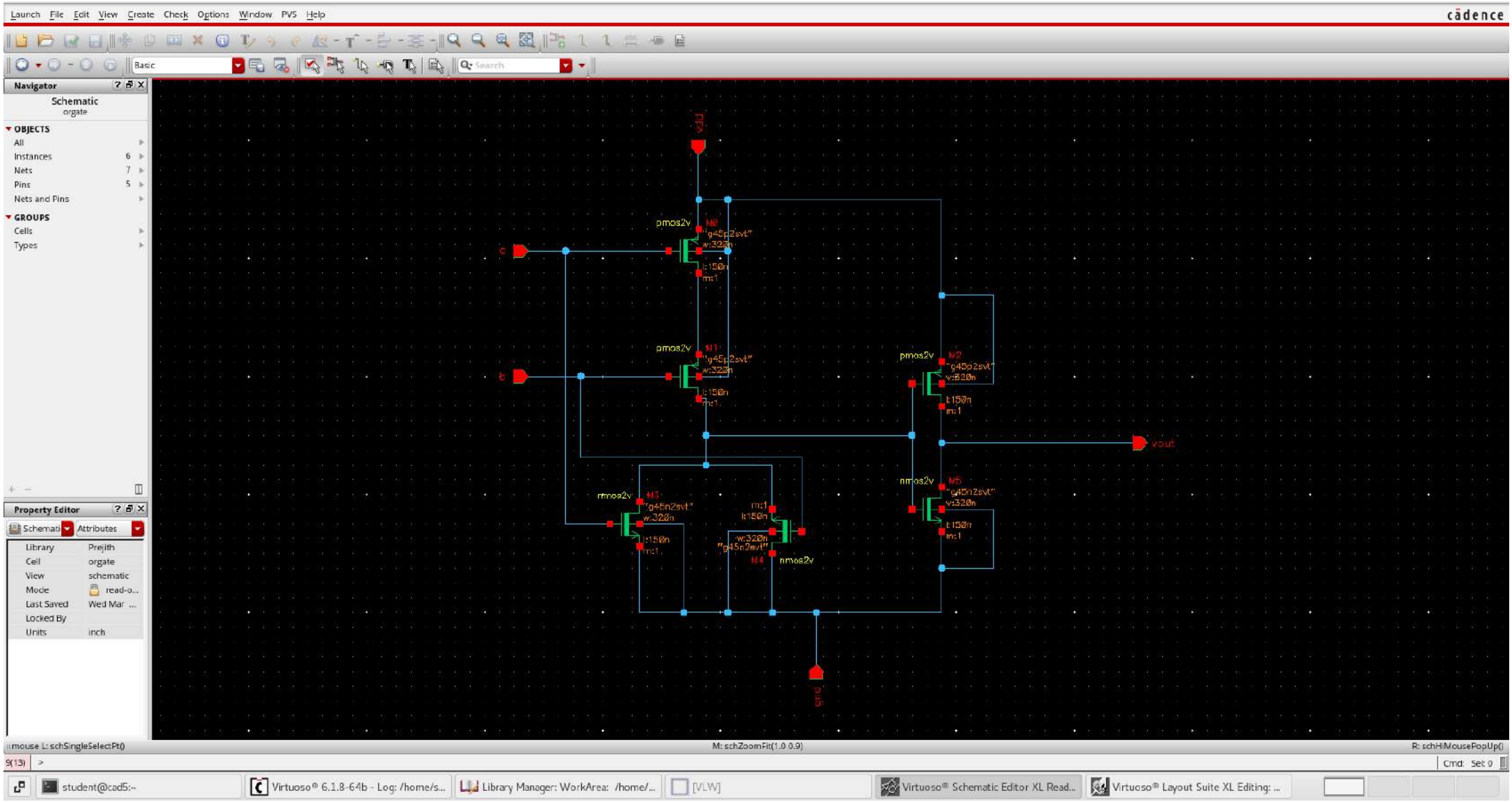
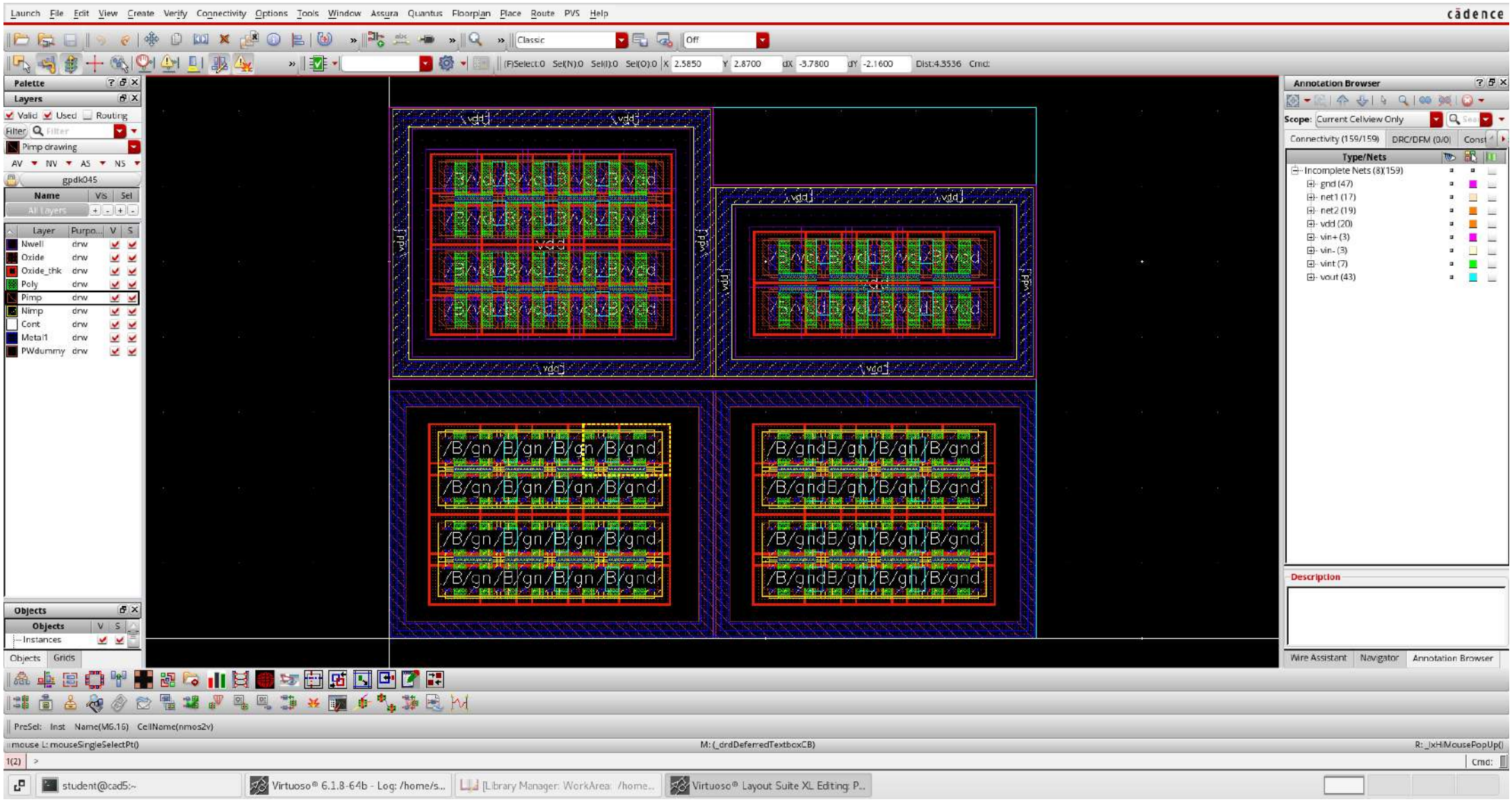
Phvt drw
Plvt drw
Nzvt drw
SIProt drw
Cont drw
Metal1 drw
Via1 drw
Metal2 drw
Via2 drw
Metal3 drw
Via3 drw
Metal4 drw
Via4 drw
Metal5 drw
Via5 drw
Metal6 drw
Via6 drw
Metal7 drw
Via7 drw
Metal8 drw
Via8 drw
Objects
Objects Grids V S
vdd
vdd
vout
net1
/b/
/a/
/B/gnd
gnd
Cmd:
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /home/s...
Library Manager: WorkArea: /home/...
[VLW]
Virtuoso® Layout Suite XL Editing: ...


Run: "orgate"
Run: "orgate" from
/home/student/
Schematic and Layout Match.
You currently have an open run (project).
Do you want to close current project and view the results of new run?
Summary of LVS Issues
Extraction Information:
0 cells have 0 mal-formed device problems
0 cells have 0 label short problems
0 cells have 0 label open problems
Comparison Information:
0 cells have 0 Net mismatches
0 cells have 0 Device mismatches
0 cells have 0 Pin mismatches
0 cells have 0 Parameter mismatches
ELW Information:
Total DRC violations: 0
Yes
No
Help
vdd
gnd
/B/gnd
Run: "orgate"


Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Valid Used Routing
Nimp drawing
gpdk045
Name Vis Sel
Layer
Nwell
Oxide
Oxide_thk
Poly
Pimp
Nhvt
Nlvt
Nimp
Phvt
Plvt
Nzvt
SiProt
Cont
Metal1
Via1
Metal2
Via2
Metal3
Via3
Metal4
Via4
Metal5
Via5
Metal6
Via6
Metal7
Via7
Metal8
Via8
Objects
vdd
/vdd/
net1
/a/
/B/gnd /B/gnd /B/gnd3
gnd
/gnd/
mouse L: mouseSingleSelectPt()
M: (_drdDeferredTextboxCB)
R: _lxHiMousePopUp()
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /home/s...
Library Manager: WorkArea: /home/...
Virtuoso® Schematic Editor XL Read...
complimentarycircuit
Virtuoso® Layout Suite XL Editing: ...


Launch File Edit View Create Check Options Window PVS Help
cadence
Navigator
Schematic
orgate
OBJECTS
All
Instances 6
Nets 7
Pins 5
Nets and Pins
GROUPS
Cells
Types
Property Editor
Schemati
Attributes
Library Prejith
Cell orgate
View schematic
Mode read-o...
Last Saved Wed Mar ...
Locked By
Units inch
vdd
gnd
vout
pmos2v
nmos2v
M0
M1
M2
M3
M4
M5
mouse L: schSingleSelectPt()
M: schZoomFit(1.0 0.9)
R: schHiMousePopUp()
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /home/s...
Library Manager: WorkArea: /home/...
[VLW]
Virtuoso® Schematic Editor XL Read...
Virtuoso® Layout Suite XL Editing: ...


Launch File Edit View Create Verify Connectivity Options Tools Window Assura Quantus Floorplan Place Route PVS Help
cadence
Palette
Layers
Valid Used Routing
Filter
Pimp drawing
gpdk045
Name Vis Sel
All Layers
Layer Purpo... V S
Nwell drw
Oxide drw
Oxide_thk drw
Poly drw
Pimp drw
Nimp drw
Cont drw
Metal1 drw
PWdummy drw
Objects
Objects V S
Instances
Objects Grids
vdd
/B/gn/B/gn/B/gn/B/gnd
/B/gndB/gn/B/gn/B/gnd
Annotation Browser
Scope: Current Celview Only
Connectivity (159/159) DRC/DFM (0/0) Const
Type/Nets
Incomplete Nets (8)(159)
gnd (47)
net1 (17)
net2 (19)
vdd (20)
vin+ (3)
vin- (3)
vint (7)
vout (43)
Description
Wire Assistant Navigator Annotation Browser
PreSel: Inst Name(M6.16) CellName(nmos2v)
mouse L: mouseSingleSelectPt()
M: (_drdDeferredTextboxCB)
R: _lxHiMousePopUp()
Cmd:
student@cad5:~
Virtuoso® 6.1.8-64b - Log: /home/s...
[Library Manager: WorkArea: /home...
Virtuoso® Layout Suite XL Editing: P...